

STIC Database Tracking Number: 346617

To: JOHN ANDERSON
Location: KNX-4A48
Art Unit: 3694
Monday, November 01, 2010

Case Serial Number: 10/781377

From: ROBERT FINLEY
Location: EIC3600
KNX-2A80-C
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Search Notes

Dear Examiner Anderson:

Please find attached the results of your search for the above-referenced case. The search was conducted in the Business Methods Template databases appropriate for the application.

I have listed *potential* references of interest in the first part of the search results. However, please be sure to scan through the entire report. There may be additional references that you might find useful.

Dialog search results are presented in two formats, Word (.doc) and Acrobat (.pdf).

Information on Dialog databases can be found at: <http://library.dialog.com/bluesheets/>

If you have any questions about the search, or need a refocus, please do not hesitate to contact me.

Thank you for using the EIC, and we look forward to your next search.

I.	POTENTIAL REFERENCES OF INTEREST	3
A.	Dialog	3
B.	Additional Resources Searched	8
II.	INVENTOR SEARCH RESULTS FROM DIALOG.....	9
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I. Potential References of Interest

A. Dialog

Non-Patent Literature: Full Text

7/3,K/2 (Item 1 from file: 15)
DIALOG(R)File 15:ABI/Inform(R)
(c) 2010 ProQuest Info&Learning. All rts. reserv.

02161127 73027407
Flipping for FlipFactory
Perey, Christine
Network World v18n20 PP: 49-50 May 14, 2001
ISSN: 0887-7661 JRNL CODE: NWW
WORD COUNT: 2099

...TEXT: audio media from one format - a high-quality, low-compressed or uncompressed original, (such as from a digital video editing system) - to another format suitable for IP-based streaming (or from one format to multiple data rates and formats). Industry-leading solutions for performing these conversions (most notably Media 100's Cleaner Pro and Avid's ePublisher) are designed...

...same person who edits, titles and "polishes" the content to meet corporate communications guidelines typically will be involved in the conversion to Web-ready/streaming format (also known as encoding).

Many media professionals spend hours bent over a keyboard and mouse in an editing bay...

7/3,K/22 (Item 5 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
(c) 2010 Gale/Cengage. All rts. reserv.

01174422 SUPPLIER NUMBER: 04240833
Micro to mainframe: making the right connection.
Derfler, Frank J.Jr.
PC Week, v5, n9, p116(6)
May 13, 1986
ISSN: 0740-1604 LANGUAGE: ENGLISH RECORD TYPE: ABSTRACT

...ABSTRACT: before loading it back to the host to be recompiled and run; and data exchange. The problems in connecting the two different

computer systems include the data arrangement on the communications line and the data alphabet used by the host, linking synchronous (host) and asynchronous transmissions, and handling simultaneous translations and conversions. Potential solutions to these problems and the outlook for micro-to-mainframe links are discussed.

Patent Literature: Full Text

5/3,K/2 (Item 2 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
(c) 2010 European Patent Office. All rts. reserv.

01176966

Dynamically configurable system and method for transcoding streaming data and telecommunications infrastructure incorporating the same
Dynamisch konfigurierbares System und Verfahren zur Transkodierung eines Datenstromes und einer Telekommunikationsinfrastruktur mit denselben Funktionen

Systeme et methode configurables dynamiques pour transcodage des d'un train de donnees et infrastructure telecommunication les incorporant

PATENT ASSIGNEE:

LUCENT TECHNOLOGIES INC., (2143720), 600 Mountain Avenue, Murray Hill, New Jersey 07974-0636, (US), (Applicant designated States: all)

INVENTOR:

Bouis, Jeffrey D., 9340 Sunset Drive, Fisco, Texas 75034, (US)

Sherer, Jeffrey A., 922 Dunbarton Drive, Richarson, Texas 75081, (US)

LEGAL REPRESENTATIVE:

Williams, David John et al (86433), Page White & Farrer, 54 Doughty Street, London WC1N 2LS, (GB)

PATENT (CC, No, Kind, Date): EP 1026872 A1 000809 (Basic)

APPLICATION (CC, No, Date): EP 300375 000119;

PRIORITY (CC, No, Date): US 239286 990129

DESIGNATED STATES: DE; FR; GB

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS (V7): H04M-007/00

ABSTRACT WORD COUNT: 113

NOTE:

Figure number on first page: 4

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200032	674
SPEC A	(English)	200032	3584
Total word count - document A			4258

Total word count - document B 0
Total word count - documents A + B 4258

...ABSTRACT a telecommunications infrastructure incorporating the system or the method. In one embodiment, the system includes: (1) first, second and third **streaming conversion** modules capable of **converting** data stream portions between internal **data formats** and (2) a **transcoding controller**, associated with the first, second and third **streaming conversion** modules, that determines source and destination **data formats** of the data stream and arranges ones of the first, second and third **streaming conversion** modules in an optimal series to convert portions of the data stream from the source **data format** through at least one of the internal data formats to the destination data format.

NOTE:

...SPECIFICATION a telecommunications infrastructure incorporating the system or the method. In one embodiment, the system includes: (1) first, second and third **streaming conversion** modules capable of **converting** data stream portions between internal **data formats** and (2) a **transcoding controller**, associated with the first, second and third **streaming conversion** modules, that determines source and destination **data formats** of the data stream and arranges ones of the first, second and third **streaming conversion** modules in an optimal series to convert portions of the data stream from the source **data format** through at least one of the internal data formats to the destination data format.

The present invention therefore introduces a...dynamically configurable system and method for transcoding streaming data and a telecommunications infrastructure incorporating the system or the method. In one embodiment, the system includes: (1) first, second and third **streaming conversion** modules capable of **converting** data stream portions between internal **data formats** and (2) a **transcoding controller**, associated with the first, second and third **streaming conversion** modules, that determines source and destination **data formats** of the data stream and arranges ones of the first, second and third **streaming conversion** modules in an optimal series to convert portions of the data stream from the source **data format** through at least one of the internal data formats to the destination data format.

Although the present invention has been...

5/3,K/7 (Item 7 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2010 Thomson Reuters. All rts. reserv.

0013359507 - Drawing available

WPI ACC NO: 2003-447577/200342

XRPX Acc No: N2003-356917

Data files conversion system in computer automated document and file management system, includes multiple data processing devices which are programmed to convert files from various format into common format

Patent Assignee: DZIENIS A (DZIE-I)

Inventor: DZIENIS A

Patent Family (1 patents, 1 countries)

Patent Application

Number	Kind	Date	Number	Kind	Date	Update
US 20030037302	A1	20030220	US 2001300662	P	20010624	200342 B
			US 2002177953	A	20020621	

Priority Applications (no., kind, date): US 2001300662 P 20010624; US
2002177953 A 20020621

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
US 20030037302	A1	EN	9	3	Related to Provisional US 2001300662

Original Publication Data by Authority

Argentina

Assignee name & address:

Original Abstracts:

Systems and methods provide parallel processing for simultaneously converting a plurality of files into various file formats into a common file format. Electronic storage media containing multiple files in various file formats is made accessible to a plurality of personal computers connected through a network. The plurality of computers simultaneously converts the files into a common format for storage.

Claims:

5/3,K/14 (Item 14 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2010 Thomson Reuters. All rts. reserv.

0010247627 - Drawing available

WPI ACC NO: 2000-559592/200052

XRFX Acc No: N2000-414159

Dynamically configurable system for transcoding streaming data in a telecommunication infrastructure with computers using different formats to store and communicate between users

Patent Assignee: LUCENT TECHNOLOGIES INC (LUCE); AVAYA TECHNOLOGY CORP (AVAY)

Inventor: BOUIS J D; SHERER J A

Patent Family (5 patents, 28 countries)

Patent			Application			
Number	Kind	Date	Number	Kind	Date	Update
EP 1026872	A1	20000809	EP 2000300375	A	20000119	200052 B
CA 2296179	A1	20000729	CA 2296179	A	20000117	200054 E
JP 2000236386	A	20000829	JP 200019736	A	20000128	200056 E
US 6741608	B1	20040525	US 1999239286	A	19990129	200435 E
CA 2296179	C	20050830	CA 2296179	A	20000117	200558 E

Priority Applications (no., kind, date): US 1999239286 A 19990129

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing	Notes
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EP 1026872	A1	EN	15	5		
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Regional Designated States,Original: AL AT BE CH CY DE DK ES FI FR GB GR
IE IT LI LT LU LV MC MK NL PT RO SE SI

CA 2296179	A1	EN				
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JP 2000236386	A	JA	9			
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CA 2296179	C	EN				
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Original Publication Data by Authority

Argentina

Assignee name & address:

Original Abstracts:

...dynamically configurable system and method for transcoding streaming data and a telecommunications infrastructure incorporating the system or the method. In one embodiment, the system includes: (1)

first, second and third streaming conversion

modules capable of **converting** data stream portions between internal data formats and (2) a transcoding controller, associated

with the **first, second and third streaming conversion**

modules, that determines source and destination data formats of

the data stream and arranges ones of the first, second and third streaming conversion modules in an optimal series...

...dynamically configurable system and method for transcoding streaming data and a telecommunications infrastructure incorporating the system or the method. In one embodiment, the system includes: (1)

first, second and third streaming conversion modules capable of converting data stream portions between internal data formats and (2) a transcoding controller, associated with the first, second and third streaming conversion modules, that determines source and destination data formats of the data stream and arranges ones of the first, second and third streaming conversion modules in an optimal series...

Claims:

A system for transcoding a data stream, comprising: first, second and third streaming conversion modules capable of converting data stream portions between internal data formats; and a transcoding controller, associated with said first, second and third streaming conversion modules, that determines source and destination data formats of said data stream and arranges ones of said first, second and third streaming conversion modules in an optimal series...

B. Additional Resources Searched

Nothing of interest found.

II. Inventor Search Results from Dialog

Patent Literature: Inventor search

File 347:JAPIO Dec 1976-2010/Jul(Updated 101027)
(c) 2010 JPO & JAPIO
File 348:EUROPEAN PATENTS 1978-201042
(c) 2010 European Patent Office
File 349:PCT FULLTEXT 1979-2010/UB=20101028|UT=20101021
(c) 2010 WIPO/Thomson
File 350:Derwent WPIX 1963-2010/UD=201069
(c) 2010 Thomson Reuters

Set	Items	Description
S1	521	AU=DUNCAN R?
S2	0	AU=OXLER K?
S3	20	AU=WHIPPLE S?
S4	541	S1 OR S2 OR S3
S5	0	S4 AND ((CONVERT? OR CONVERSION? ? OR TRANSLAT? OR REFORMA- T? OR RECONFIGUR? OR TRANSFORM? OR TRANSPOS? OR PARSE? ? OR P- ARSING)(3N)(STREAMING OR PARALLEL? OR CONTEMPORANE? OR SYNCHR- ON? OR CONCURRENT? OR COINCIDENT? OR SIMULTANE?))(S)((DATA OR FILE)(4N)(FORMAT? ? OR FORMATT?))

Non-Patent Literature: Inventor search

File 2:INSPEC 1898-2010/Oct W4
(c) 2010 The IET
File 9:Business & Industry(R) Jul/1994-2010/Oct 29
(c) 2010 Gale/Cengage
File 13:BAMP 2010/Oct 29
(c) 2010 Gale/Cengage
File 15:ABI/Inform(R) 1971-2010/Oct 30
(c) 2010 ProQuest Info&Learning
File 16:Gale Group PROMT(R) 1990-2010/Oct 28
(c) 2010 Gale/Cengage
File 20:Dialog Global Reporter 1997-2010/Nov 01
(c) 2010 Dialog
File 35:Dissertation Abs Online 1861-2010/Sep
(c) 2010 ProQuest Info&Learning
File 65:Inside Conferences 1993-2010/Oct 29
(c) 2010 BLDSC all rts. reserv.
File 75:TGG Management Contents(R) 86-2010/Oct W4
(c) 2010 Gale/Cengage
File 95:TEME-Technology & Management 1989-2010/Sep W3

(c) 2010 FIZ TECHNIK
 File 99:Wilson Appl. Sci & Tech Abs 1983-2010/Aug
 (c) 2010 The HW Wilson Co.
 File 148:Gale Group Trade & Industry DB 1976-2010/Oct 29
 (c) 2010 Gale/Cengage
 File 160:Gale Group PROMT(R) 1972-1989
 (c) 1999 The Gale Group
 File 256:TecTrends 1982-2010/Oct W3
 (c) 2010 Info.Sources Inc. All rights res.
 File 275:Gale Group Computer DB(TM) 1983-2010/Sep 17
 (c) 2010 Gale/Cengage
 File 474:New York Times Abs 1969-2010/Oct 31
 (c) 2010 The New York Times
 File 475:Wall Street Journal Abs 1973-2010/Nov 01
 (c) 2010 The New York Times
 File 583:Gale Group Globalbase(TM) 1986-2002/Dec 13
 (c) 2002 Gale/Cengage
 File 610:Business Wire 1999-2010/Nov 01
 (c) 2010 Business Wire.
 File 613:PR Newswire 1999-2010/Nov 01
 (c) 2010 PR Newswire Association Inc
 File 621:Gale Group New Prod.Annou.(R) 1985-2010/Sep 08
 (c) 2010 Gale/Cengage
 File 624:McGraw-Hill Publications 1985-2010/Oct 29
 (c) 2010 McGraw-Hill Co. Inc
 File 634:San Jose Mercury Jun 1985-2010/Oct 31
 (c) 2010 San Jose Mercury News
 File 636:Gale Group Newsletter DB(TM) 1987-2010/Oct 27
 (c) 2010 Gale/Cengage
 File 647:UBM Computer Fulltext 1988-2010/Oct W4
 (c) 2010 UBM, LLC
 File 674:Computer News Fulltext 1989-2006/Sep W1
 (c) 2006 IDG Communications
 File 810:Business Wire 1986-1999/Feb 28
 (c) 1999 Business Wire
 File 813:PR Newswire 1987-1999/Apr 30
 (c) 1999 PR Newswire Association Inc

Set	Items	Description
S1	5848	AU=(DUNCAN, R? OR DUNCAN R? OR DUNCAN(2N)R?)
S2	2	AU=(OXLER, K? OR OXLER K? OR OXLER(2N)?)
S3	35	AU=(WHIPPLE, S? OR WHIPPLE S? OR WHIPPLE(2N)?)
S4	5885	S1 OR S2 OR S3
S5	0	S4 AND ((CONVERT? OR CONVERSION? ? OR TRANSLAT? OR REFORMA- T? OR RECONFIGUR? OR TRANSFORM? OR TRANSPOS? OR PARSE? ? OR P- ARSING)(3N)(STREAMING OR PARALLEL? OR CONTEMPORANE? OR SYNCHR- ON? OR CONCURRENT? OR COINCIDENT? OR SIMULTANE?))(S)((DATA OR

FILE) (4N) (FORMAT? ? OR FORMATT?))

III. Text Search Results from Dialog

A. Patent Files, Full-text

Patent Literature: Full Text

Dialog files: 348,349

File 348:EUROPEAN PATENTS 1978-201043

(c) 2010 European Patent Office

File 349:PCT FULLTEXT 1979-2010/UB=20101028|UT=20101021

(c) 2010 WIPO/Thomson

Set	Items	Description
S1	2112453	(CONVERT? OR CONVERSION? ? OR TRANSLAT? OR REFORMAT? OR RE- CONFIGUR? OR RESTRUCTUR? OR TRANSFORM? OR TRANSPOS? OR PARSE? ? OR PARSING OR EXTRACT? OR DERIV? OR FILTER?)
S2	114361	S1(5N)(STREAMING OR PARALLEL? OR CONTEMPORANE? OR SYNCHRON? OR (SAME OR ONE)() (TIME OR INSTANT OR MOMENT) OR IMMEDIAT? OR CONCURRENT? OR COINCIDENT? OR SIMULTANE?)
S3	16963	(DATA OR FILE)(1N)(FORMAT? ? OR FORMATT? OR STRUCTUR? OR M- ODEL? OR PARADIGM? OR STANDARD? OR CONFIGUR? OR PATTERN? ? OR ARRANG?)
S4	60586	(FIRST OR ONE OR PRIME OR PRIMARY OR SECOND? OR TWO OR DIF- FER? OR SEPARAT? OR DISTINCT? OR PLURAL? OR MULTIPLE? ? OR MU- LTI OR ANOTHER)(3N)(SYSTEM? ? OR PLATFORM? ? OR COMPUTER? ? OR MACHINE? ? OR EQUIPMENT OR CONTROLLER?)
S5	25	S2(8N)S3(8N)S4

5/3,K/1 (Item 1 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

(c) 2010 European Patent Office. All rts. reserv.

01318489

A network portal system and methods

Netzwerkzugangssystem und -verfahren

Portique de reseau et procede associe

PATENT ASSIGNEE:

Sun Microsystems, Inc., (1392738), 901 San Antonio Road, Palo Alto,
California 94303-4900, (US), (Applicant designated States: all)

INVENTOR:

Hutsch, Matthias, Hertogestr. 14, 22111 Hamburg, (DE)

Hofmann, Ralf, Schmahlsweg 3, 22143 Hamburg, (DE)

Sommerfeld, Kai, Vossdrift 4, 21149 Hamburg, (DE)

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Eilers, Bernd, Vogelhuttendeich 29, 21107 Hamburg, (DE)

Pfohe, Thomas, Wariner Weg 1, 22143 Hamburg, (DE)

Honnig, Michael, Boytinstr. 10, 22143 Hamburg, (DE)

Meyer, Markus, Winsener Landstr. 26, 21423 Winsen/Luhe, (DE)
LEGAL REPRESENTATIVE:
HOFFMANN - EITLE (101511), Patent- und Rechtsanwälte Arabellastrasse 4,
81925 Munchen, (DE)
PATENT (CC, No, Kind, Date): EP 1126681 A2 010822 (Basic)
APPLICATION (CC, No, Date): EP 2001100131 010115;
PRIORITY (CC, No, Date): EP 2000100738 000114; EP 2000100211 000114; EP
2000100740 000114; EP 2000100212 000114; EP 2000100739 000114
DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI;
LU; MC; NL; PT; SE; TR
EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI
INTERNATIONAL PATENT CLASS (V7): H04L-029/06; H04L-029/12
ABSTRACT WORD COUNT: 142
NOTE:

Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200134	3891
SPEC A	(English)	200134	139489
Total word count - document A			143380
Total word count - document B			0
Total word count - documents A + B			143380

...SPECIFICATION the MIME type of the retrieved data to a MIME type that
can be displayed by user device 102i. In one embodiment, the
retrieved presentation scheme is loaded in a memory accessible by portlet
manager 321. Retrieve scheme operation 444 transfers...

5/3,K/2 (Item 2 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
(c) 2010 European Patent Office. All rts. reserv.

01176966

Dynamically configurable system and method for transcoding streaming data
and telecommunications infrastructure incorporating the same
Dynamisch konfigurierbares System und Verfahren zur Transkodierung eines
Datenstromes und einer Telekommunikationsinfrastruktur mit denselben
Funktionen

Systeme et methode configurables dynamiques pour transcodage des d'un train
de donnees et infrastructure telecommunication les incorporant
PATENT ASSIGNEE:

LUCENT TECHNOLOGIES INC., (2143720), 600 Mountain Avenue, Murray Hill,
New Jersey 07974-0636, (US), (Applicant designated States: all)
INVENTOR:

Bouis, Jeffrey D., 9340 Sunset Drive, Fisco, Texas 75034, (US)
Sherer, Jeffrey A., 922 Dunbarton Drive, Richardson, Texas 75081, (US)
LEGAL REPRESENTATIVE:

Williams, David John et al (86433), Page White & Farrer, 54 Doughty
Street, London WC1N 2LS, (GB)

PATENT (CC, No, Kind, Date): EP 1026872 A1 000809 (Basic)

APPLICATION (CC, No, Date): EP 300375 000119;

PRIORITY (CC, No, Date): US 239286 990129

DESIGNATED STATES: DE; FR; GB

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS (V7): H04M-007/00

ABSTRACT WORD COUNT: 113

NOTE:

Figure number on first page: 4

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200032	674
SPEC A	(English)	200032	3584
Total word count - document A			4258
Total word count - document B			0
Total word count - documents A + B			4258

...ABSTRACT a telecommunications infrastructure incorporating the system or the method. In one embodiment, the system includes: (1) first, second and third **streaming conversion** modules capable of **converting** data stream portions between internal **data formats** and (2) a transcoding **controller**, associated with the **first**, second and third **streaming conversion** modules, that determines source and destination **data formats** of the data stream and arranges ones of the first, second and third **streaming conversion** modules in an optimal series to convert portions of the data stream from the source **data format** through at least one of the internal data formats to the destination data format.

NOTE:

...SPECIFICATION a telecommunications infrastructure incorporating the system or the method. In one embodiment, the system includes: (1) first, second and third **streaming conversion** modules capable of **converting** data stream portions between internal **data formats** and (2) a transcoding **controller**, associated with the **first**, second and third **streaming conversion** modules, that determines source and destination **data formats** of the data stream and arranges ones of the first, second and third **streaming conversion** modules in an optimal series to convert

portions of the data stream from the source **data format** through at least one of the internal data formats to the destination data format.

The present invention therefore introduces a...dynamically configurable system and method for transcoding streaming data and a telecommunications infrastructure incorporating the system or the method. In one embodiment, the system includes: (1) first, **second** and third **streaming conversion** modules capable of **converting** data stream portions between internal **data formats** and (2) a **transcoding controller**, associated with the **first**, second and third **streaming conversion** modules, that determines source and destination **data formats** of the data stream and arranges ones of the first, second and third **streaming conversion** modules in an optimal series to convert portions of the data stream from the source **data format** through at least one of the internal data formats to the destination data format.

Although the present invention has been...

5/3,K/3 (Item 3 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
(c) 2010 European Patent Office. All rts. reserv.

00952646

Printer driver switching in windows operating systems
Geschalteter Druckertreiber in Windows-Betriebssystem
Commutation de driver d'imprimante dans un systeme d'exploitation windows
PATENT ASSIGNEE:

Hewlett-Packard Company, A Delaware Corporation, (3016020), 3000 Hanover
Street, Palo Alto, CA 94304, (US), (Proprietor designated states: all)
INVENTOR:

Snyders, Lawrence M., 1019 W. Highland View Drive, Boise, Idaho 83702,
(US)

LEGAL REPRESENTATIVE:

Schoppe, Fritz, Dipl.-Ing. (55463), Schoppe, Zimmermann, Stockeler &
Zinkler Patentanwalte Postfach 246, 82043 Pullach bei Munchen, (DE)
PATENT (CC, No, Kind, Date): EP 864964 A2 980916 (Basic)
EP 864964 A3 000524
EP 864964 B1 040303

APPLICATION (CC, No, Date): EP 97116646 970924;

PRIORITY (CC, No, Date): US 816978 970313

DESIGNATED STATES: DE; FR; GB

EXTENDED DESIGNATED STATES: AL; LT; LV; RO; SI

INTERNATIONAL PATENT CLASS (V7): G06F-003/12

ABSTRACT WORD COUNT: 208

NOTE:

Figure number on first page: 7

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	199838	1044
CLAIMS B	(English)	200410	1056
CLAIMS B	(German)	200410	1002
CLAIMS B	(French)	200410	1261
SPEC A	(English)	199838	6679
SPEC B	(English)	200410	6349
Total word count - document A			7724
Total word count - document B			9668
Total word count - documents A + B			17392

...SPECIFICATION document US 5,511,156 teaches a print job distribution system in which print files are described in a postscript file format, said postscript print file being divided into independent portions for parallel translation processing by multiple computers.

Therefore, a need exists for a system that distributes print jobs from a computer operating within a computer network environment...

5/3,K/4 (Item 4 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
(c) 2010 European Patent Office. All rts. reserv.

00938459

TRANSMITTER, RECEIVER, TRANSMISSION SYSTEM, SENDING METHOD, RECEIVING METHOD, AND TRANSMITTING METHOD
SENDER, EMPFANGER, UBERTRAGUNGSSYSTEM, SENDEVERFAHREN, EMPFANGSVRFahren UND UBERTRAGUNGSVERFAHREN
EMETTEUR, RECEPTEUR, SYSTEME DE TRANSMISSION, PROCEDE D'EMISSION, PROCEDE DE RECEPTION ET PROCEDE DE TRANSMISSION
PATENT ASSIGNEE:

MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD., (216883), 1006, Oaza Kadoma, Kadoma-shi, Osaka-fu, 571, (JP), (Applicant designated States: all)

INVENTOR:

YOSHIDA, Takayasu, 6-5-5, Kibougakka Toyono-cho Toyono-gun, Osaka 563-02, (JP)

TANAKA, Masatoshi, 208 Rohreru-Higashiikoma 1-62, Higashiikoma, Ikoma-shi Nara 630-02, (JP)

NISHIOKA Minoru, 2-1-20-510, Uozaki-nishimachi Higashinada-ku, Kobe-shi Hyogo 658, (JP)

BANNAI, Tatsushi, 389-12, Kitanoda Sakai-shi, Osaka 588, (JP)

NISHINO, Masakazu, 1-4-26, Kamiichi Kashiwara-shi, Osaka 582, (JP)

LEGAL REPRESENTATIVE:

Ehlers, Jochen (87041), Eisenfuhr, Speiser & Partner, Martinistrasse 24,
28195 Bremen, (DE)

PATENT (CC, No, Kind, Date): EP 939513 A1 990901 (Basic)
WO 9810557 980312

APPLICATION (CC, No, Date): EP 97939204 970905; WO 97JP3142 970905

PRIORITY (CC, No, Date): JP 96234858 960905; JP 9745603 970228; JP 97189400
970715

DESIGNATED STATES: DE; GB; NL

RELATED DIVISIONAL NUMBER(S) - FN (AN):
(EP 2002014215)

INTERNATIONAL PATENT CLASS (V7): H04L-007/08; H04L-001/00; H03M-007/40;
H04J-003/00; H04N-007/00

ABSTRACT WORD COUNT: 158

NOTE:

Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; Japanese
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	9935	16117
SPEC A	(English)	9935	68029
Total word count - document A			84146
Total word count - document B			0
Total word count - documents A + B			84146

...SPECIFICATION FIG. 17 is a view showing data formats for one set
information unit on the receiving side of the transmission system
in accordance with the eighth embodiment of the present invention;
FIG. 18 is a view showing other data formats for one set
information unit in the transmission system in accordance with the
seventh or eighth embodiment of the present...

5/3,K/5 (Item 5 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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00420348

Cursor display control method and apparatus in a graphic display system.
Verfahren und Vorrichtung zur Steuerung der Kursordarstellung in
graphischen Anzeigesystemen.

Methode et dispositif pour commander la visualisation du curseur dans un
systeme de visualisation graphique.

PATENT ASSIGNEE:

HITACHI, LTD., (204141), 6, Kanda Surugadai 4-chome, Chiyoda-ku, Tokyo
101, (JP), (applicant designated states: DE;FR;GB;IT)
Hitachi Engineering Co., Ltd., (286321), 2-1, Saiwai-cho 3-chome,

Hitachi-shi Ibaraki-ken, (JP), (applicant designated states:
DE;FR;GB;IT)

INVENTOR:

Matsuo, Shigeru, 19-4-303, Ishinazakacho-1-chome, Hitachi-shi, (JP)
Fukushima, Tadashi, 23-5, Hanayamacho-1-chome, Hitachi-shi, (JP)
Komagawa, Toru, Tozawaryo, 10-12, Suehirocho-3-chome, Hitachi-shi, (JP)
Narita, Masahisa, 4-14-202, Jonancho-1-chome, Hitachi-shi, (JP)

LEGAL REPRESENTATIVE:

Patentanwalte Beetz - Timpe - Siegfried Schmitt-Fumian - Mayr (100712),
Steinsdorfstrasse 10, D-80538 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 418859 A1 910327 (Basic)
EP 418859 B1 950524

APPLICATION (CC, No, Date): EP 90118022 900919;

PRIORITY (CC, No, Date): JP 89242256 890920

DESIGNATED STATES: DE; FR; GB; IT

INTERNATIONAL PATENT CLASS (V7): G09G-005/08;

ABSTRACT WORD COUNT: 131

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF1	548
CLAIMS B	(English)	EPAB95	588
CLAIMS B	(German)	EPAB95	437
CLAIMS B	(French)	EPAB95	733
SPEC A	(English)	EPABF1	3729
SPEC B	(English)	EPAB95	3656
Total word count - document A			4277
Total word count - document B			5414
Total word count - documents A + B			9691

...CLAIMS said display data of said frame buffer, and a display unit (110) for displaying serial display data, wherein said display system further comprises:
 second conversion means (101) for converting said shifted parallel cursor pattern data into serial cursor pattern data at the display timing of said cursor;
 and combiner means (102) for combining the serial display data from said first...

...CLAIMS display data of said frame buffer (90), and a display unit (110) for displaying serial display data, wherein said display system further comprises:
 second conversion means (101) for converting said shifted parallel cursor pattern data into serial cursor pattern data at the display timing of said cursor;
 and

combiner means (102) for combining the serial display data from said first...

5/3,K/6 (Item 6 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
(c) 2010 European Patent Office. All rts. reserv.

00387386

Format converting system for synchronous optical network.
Formatanderungssystem für ein synchrones optisches Netzwerk.
Système de conversion de format pour un réseau optique synchrone.
PATENT ASSIGNEE:

FUJITSU LIMITED, (211460), 1015, Kamikodanaka Nakahara-ku, Kawasaki-shi
Kanagawa 211, (JP), (applicant designated states: DE;FR;GB)

INVENTOR:

Eda, Hitoshi, 1440-2, Nishiishida, Shimodate-shi, Ibaragi 308, (JP)
Takaiwa, Kazumaro, 379-203, Inuzuka, Oyama-shi, Tochigi 323, (JP)
Akihiro, Hayashi, 11-4 Muromachi, Tochigi-shi, Tochigi 328, (JP)

LEGAL REPRESENTATIVE:

Lehn, Werner, Dipl.-Ing. et al (7471), Hoffmann, Eitle & Partner,
Patentanwalte, Postfach 81 04 20, W-8000 München 81, (DE)

PATENT (CC, No, Kind, Date): EP 386483 A1 900912 (Basic)
EP 386483 B1 930811

APPLICATION (CC, No, Date): EP 90102403 900207;

PRIORITY (CC, No, Date): JP 8927488 890208

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS (V7): H04J-003/00; H04J-003/06; H04L-007/00;
ABSTRACT WORD COUNT: 231

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	1730
CLAIMS B	(German)	EPBBF1	1488
CLAIMS B	(French)	EPBBF1	2016
SPEC B	(English)	EPBBF1	8139
Total word count - document A			0
Total word count - document B			13373
Total word count - documents A + B			13373

...SPECIFICATION to provide a format converting system capable of executing format conversion by a low-speed circuit without causing the erroneous order of data bits.

The above objects of the present invention are achieved by a format converting system comprising first converting means for converting an input signal into N

parallel signals (N is an integer), each of the N parallel signals having a bit rate less than that of the input signal; latch means, operatively coupled to the first converting means...

5/3,K/7 (Item 7 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
(c) 2010 European Patent Office. All rts. reserv.

00316864

Digital signal recording/reproducing apparatus.
Vorrichtung zum Aufnehmen und Wiedergeben von digitalen Signalen.
Dispositif d'enregistrement et de reproduction d'un signal numerique.
PATENT ASSIGNEE:

MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD., (216883), 1006, Oaza Kadoma,
Kadoma-shi, Osaka-fu, 571, (JP), (applicant designated states:
DE;FR;GB;NL)

INVENTOR:

Tanaka, Hiroshi, 103, Todacho-5-chome, Moriguchi-shi, (JP)
Ishiwata, Tetsuo, 12-406, Senriyamakirigaoka, Suita-shi, (JP)
Hitotsumachi, Shuzo, 16-13, Otokoyamamizajkura, Yawata-shi, (JP)
Komae, Hitoshi, 5-6-1308, Tomobuchicho-1-chome, Miyakojima-ku Osaka, (JP)
Yamaguchi, Susumu, Famirumoriguchi 1208, Yagumohigashimachi-2-chome
Moriguchi-shi, (JP)
Yoshino, Tadashi, 28-5, Kasugacho, Neyagawa-shi, (JP)
Yamauchi, Eiiji, Shoeiryō 30-23, Miyukihigashimachi Neyagawa-shi, (JP)

LEGAL REPRESENTATIVE:

Sorrell, Terence Gordon et al (36145), Fitzpatrick's Cardinal Court 23,
Thomas More Street, London E1 9YY, (GB)

PATENT (CC, No, Kind, Date): EP 310330 A2 890405 (Basic)
EP 310330 A3 900808
EP 310330 B1 930908

APPLICATION (CC, No, Date): EP 88308932 880927;

PRIORITY (CC, No, Date): JP 87245107 870929; JP 87252892 871007; JP
87296933 871124

DESIGNATED STATES: DE; FR; GB; NL

INTERNATIONAL PATENT CLASS (V7): H04N-005/91; H04N-005/92;
ABSTRACT WORD COUNT: 98

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	372
CLAIMS B	(German)	EPBBF1	300
CLAIMS B	(French)	EPBBF1	496
SPEC B	(English)	EPBBF1	4265

Total word count - document A	0
Total word count - document B	5433
Total word count - documents A + B	5433

...SPECIFICATION and time-compresses the m data block header words during a period (data period) other than the predeterminedamble period, said modulator includes a serial/parallel converter for converting the output of said formatting controller to 2-bit parallel data, first and second NRZI converters for NRZI-converting the 2-bit output of the serial/parallel converter and a QPSK modulator for four-phase modulating or offset four-phase modulating the outputs of the

...CLAIMS receiving the data output of the time axis converter and encoding the data output with a predetermined error correction;
a formatting controller (4) for adding a header word including a synchronisation signal and a rate discrimination signal to each of m data blocks in one data frame of the data output of said encoder;
a recorder (6) including a modulator (5) for modulating the output...

5/3,K/8 (Item 8 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
(c) 2010 European Patent Office. All rts. reserv.

00306062
Digital data processing system.
Digitales Datenverarbeitungssystem.
Systeme du traitement de donnees numeriques.
PATENT ASSIGNEE:

DATA GENERAL CORPORATION, (410940), Route 9, Westboro Massachusetts 01581, (US), (applicant designated states: AT;BE;CH;DE;FR;GB;IT;LI;LU;NL;SE)
INVENTOR:
Bratt, Richard Glenn, 9 Brook Trail Road, Wayland Massachusetts 01778, (US)
Clancy, Gerald F., 13069 Jaccaranda Center, Saratoga California 95070, (US)
Gavrin, Edward S., Beaver Pond Road RFD 4, Lincoln Massachusetts 01773, (US)
Gruner, Ronald Hans, 112 Dublin Wood Drive, Cary North Carolina 27514, (US)
Mundie, Craig James, 136 Castlewood Drive, Cary North Carolina, (US)
Schleimer, Stephen I., 1208 Ellen Place, Chapel Hill North Carolina 27514, (US)

Wallach, Steven J., 12436 Green Meadow Lane, Saratoga California 95070,
(US)

LEGAL REPRESENTATIVE:

Robson, Aidan John et al (69471), Reddie & Grose 16 Theobalds Road,
London WC1X 8PL, (GB)

PATENT (CC, No, Kind, Date): EP 300516 A2 890125 (Basic)
EP 300516 A3 890426
EP 300516 B1 931124

APPLICATION (CC, No, Date): EP 88200921 820521;

PRIORITY (CC, No, Date): US 266413 810522; US 266539 810522; US 266521
810522; US 266415 810522; US 266409 810522; US 266424 810522; US 266421
810522; US 266404 810522; US 266414 810522; US 266532 810522; US 266403
810522; US 266408 810522; US 266401 810522; US 266524 810522

DESIGNATED STATES: AT; BE; CH; DE; FR; GB; IT; LI; LU; NL; SE

RELATED PARENT NUMBER(S) - PN (AN):

EP 67556 (EP 823025960)

INTERNATIONAL PATENT CLASS (V7): G06F-009/46; G06F-012/14;

ABSTRACT WORD COUNT: 122

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	1018
CLAIMS B	(German)	EPBBF1	868
CLAIMS B	(French)	EPBBF1	1115
SPEC B	(English)	EPBBF1	154256
Total word count - document A			0
Total word count - document B			157257
Total word count - documents A + B			157257

...SPECIFICATION 470, 471, 472)

f.f. Failed Cross-Domain Calls (Figs. 270, 468, 469, 470, 471,
472)

6. Neighborhood Calls (Figs. 468, 469, 472)

INTRODUCTORY OVERVIEW

The following overview **will first** briefly describe the
overall **physical structure** and operation of a presently
preferred embodiment of a digital computer system incorporating the
present invention. Then certain operating features...

5/3,K/9 (Item 9 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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00306058

Digital data processing system.

Digitales Datenverarbeitungssystem.
Systeme de traitement de donnees numeriques.

PATENT ASSIGNEE:

DATA GENERAL CORPORATION, (410940), Route 9, Westboro Massachusetts 01581
, (US), (applicant designated states: AT;BE;CH;DE;FR;GB;IT;LI;LU;NL;SE)

INVENTOR:

Bachman, Brett L., 214 W. Canton Street Suite 4, Boston Massachusetts
02116, (US)
Bernstein, David H., 41 Bay Colony Drive, Ashland Massachusetts 01721,
(US)
Bratt, Richard Glenn, 9 Brook Trail Road, Wayland Massachusetts 01778,
(US)
Clancy, Gerald F., 13069 Jaccaranda Center, Saratoga California 95070,
(US)
Gavrin, Edward S., Beaver Pond Road RFD 4, Lincoln Massachusetts 01773,
(US)
Gruner, Ronald Hans, 112 Dublin Wood Drive, Cary North Carolina 27514,
(US)
Jones, Thomas M. Jones, 300 Reade Road, Chapel Hill North Carolina 27514,
(US)
Katz, Lawrence H., 10943 S. Forest Ridge Road, Oregon City Oregon 97045,
(US)
Mundie, Craig James, 136 Castlewood Drive, Cary North Carolina, (US)
Pilat, John F., 1308 Ravenhurst Drive, Raleigh North Carolina 27609, (US)
Richmond, Michael S., Fearingtn Post Box 51, Pittsboro North Carolina
27312, (US)
Schleimer Stephen I., 1208 Ellen Place, Chapel Hill North Carolina 27514,
(US)
Wallach, Steven J., 12436 Green Meadow Lane, Saratoga California 95070,
(US)
Wallach, Walter, A., Jr., 1336 Medfield Road, Raleigh North Carolina
27607, (US)

LEGAL REPRESENTATIVE:

Robson, Aidan John et al (69471), Reddie & Grose 16 Theobalds Road,
London WC1X 8PL, (GB)

PATENT (CC, No, Kind, Date): EP 290111 A2 881109 (Basic)
EP 290111 A3 890503
EP 290111 B1 931222

APPLICATION (CC, No, Date): EP 88200917 820521;

PRIORITY (CC, No, Date): US 266404 810522

DESIGNATED STATES: AT; BE; CH; DE; FR; GB; IT; LI; LU; NL; SE

RELATED PARENT NUMBER(S) - PN (AN):

EP 67556 (EP 823025960)

INTERNATIONAL PATENT CLASS (V7): G06F-009/30;

ABSTRACT WORD COUNT: 123

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	1044
CLAIMS B	(German)	EPBBF1	890
CLAIMS B	(French)	EPBBF1	1185
SPEC B	(English)	EPBBF1	154314
Total word count - document A			0
Total word count - document B			157433
Total word count - documents A + B			157433

...SPECIFICATION generate and manipulate, for example, entries for MHT 10716, MFT 10718, AOT 10712, and AST 10914, and other CS 10110 data structures.

OFFMUXOS 23822 is a multiplexer having first, second, and third inputs from, respectively, BIAS 20246, OFFSCALE 23818, OFFIESENC 23820. OFFMUXOS 23822 may select any one of these inputs as OFFMUX 20240's second output, OFFMUX (0-31). As previously described, OFFMUX 20240's second output...

5/3,K/10 (Item 10 from file: 348)
 DIALOG(R)File 348:EUROPEAN PATENTS
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00240321

Serial communications controller.
 Serielle Übertragungssteuerungsvorrichtung.
 Dispositif de commande de communications srielles.
 PATENT ASSIGNEE:

WANG LABORATORIES INC., (333560), One Industrial Avenue, Lowell, MA 01851
 , (US), (applicant designated states: BE;DE;FR;GB)

INVENTOR:

Marzucco, Teresa, 144 R School Street, Waltham, MA 02154, (US)
 Martin, Patricia A., 16 Bayberry Road, Westford, MA, (US)
 Korpusik, John, 6 Bruce Street, Hudson New Hampshire 03051, (US)

LEGAL REPRESENTATIVE:

Behrens, Dieter, Dr.-Ing. et al (1701), Wuesthoff & Wuesthoff Patent- und
 Rechtsanwälte Schweigerstrasse 2, D-81541 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 239937 A2 871007 (Basic)
 EP 239937 A3 900411
 EP 239937 B1 931027

APPLICATION (CC, No, Date): EP 87104521 870326;

PRIORITY (CC, No, Date): US 846337 860331

DESIGNATED STATES: BE; DE; FR; GB

INTERNATIONAL PATENT CLASS (V7): G06F-013/42; G06F-013/28;

ABSTRACT WORD COUNT: 74

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	524
CLAIMS B	(German)	EPBBF1	460
CLAIMS B	(French)	EPBBF1	628
SPEC B	(English)	EPBBF1	2284
Total word count - document A			0
Total word count - document B			3896
Total word count - documents A + B			3896

...SPECIFICATION along a single bus 24, and one of the functions of the I/O controller is to convert between the serial data format and the parallel format used within the data processing systems whereby multiple bits are transferred in parallel as bytes.

Typically, large frames of multiple bytes are transmitted to the system and must be written into...

5/3,K/11 (Item 11 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00223782

Simultaneous filtering and background correction of chromatographic data.
Gleichzeitiges Filtern und Hintergrundkorrektur von chromatographischen
Messdaten.

Filtrage et correction de fond simultanées de données chromatographiques.

PATENT ASSIGNEE:

ESA, Inc., (595740), 45 Wiggins Avenue, Bedford Massachusetts 01730, (US)
, (applicant designated states: AT;BE;CH;DE;ES;FR;GB;IT;LI;NL;SE)

INVENTOR:

Matson, Wayne R., One Harvard Road, Ayer Massachusetts 01433, (US)
Morash, Kenneth R., 14 Mount Auburn Street, Chelmsford Massachusetts
01824, (US)

LEGAL REPRESENTATIVE:

Sommerville, John Henry et al , SOMMERVILLE & RUSHTON 11 Holywell Hill,
St. Albans Hertfordshire, AL1 1EZ, (GB)

PATENT (CC, No, Kind, Date): EP 222612 A2 870520 (Basic)
EP 222612 A3 890419

APPLICATION (CC, No, Date): EP 86308745 861111;

PRIORITY (CC, No, Date): US 797613 851113

DESIGNATED STATES: AT; BE; CH; DE; ES; FR; GB; IT; LI; NL; SE

INTERNATIONAL PATENT CLASS (V7): G01N-030/86; G01N-030/64;
ABSTRACT WORD COUNT: 157

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF1	638
SPEC A	(English)	EPABF1	2613
Total word count - document A			3251
Total word count - document B			0
Total word count - documents A + B			3251

...SPECIFICATION with different f(sub(s)) and f(sub(p)) filter pairs:
- Initially, the test data is digitally filtered by a (Data General) Model 10SP computer in two separate runs. An auxiliary processor board is used to do dual digital filtering simultaneously with a dual board capability.
- The I/E cutoff frequency for a 0.1 second filter response is set at...

5/3,K/12 (Item 12 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
(c) 2010 European Patent Office. All rts. reserv.

00219275
Automatic flight control systems.
Digitales automatisches Flugsteuersystem.
Systeme numerique de commande automatique de vol.
PATENT ASSIGNEE:

HONEYWELL INC., (246050), Honeywell Plaza, Minneapolis Minnesota 55408,
(US), (applicant designated states: DE;FR;GB;IT;NL)

INVENTOR:
Davidson, Dale Dean, 7027 West Oregon Avenue, Glendale Arizona 85303,
(US)
Endrud, Douglas Gene, 15241 North 53rd Street, Glendale Arizona 85306,
(US)

LEGAL REPRESENTATIVE:

Singleton, Jeffrey et al (35912), Eric Potter & Clarkson St. Mary's Court
St. Mary's Gate, Nottingham NG1 1LE, (GB)
PATENT (CC, No, Kind, Date): EP 205274 A2 861217 (Basic)
EP 205274 A3 890322
EP 205274 B1 930217

APPLICATION (CC, No, Date): EP 86303985 860527;
PRIORITY (CC, No, Date): US 743535 850611
DESIGNATED STATES: DE; FR; GB; IT; NL
INTERNATIONAL PATENT CLASS (V7): G06F-015/50; G06F-011/16; G06F-015/16;
ABSTRACT WORD COUNT: 181

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPABF1	665
SPEC B	(English)	EPABF1	4080
Total word count - document A			0
Total word count - document B			4745
Total word count - documents A + B			4745

...ABSTRACT having two digital processors (10;11) receives sensor data over a bit serial data bus (20) through a serial-to-parallel converter (22). The converter (22) formats the data into bytes corresponding to the data parameters from the sensors. One of the processors (10) controls the bus timing and...

NOTE:

5/3,K/13 (Item 13 from file: 348)
 DIALOG(R)File 348:EUROPEAN PATENTS
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00182678

Method and apparatus for controlling digital voice recording and playback over telephone lines and adapted for use with any host computer.

Verfahren und Gerat zur Steuerung der Aufnahme und Wiedergabe von digitaler Sprache uber Fernsprechleitungen und angepasst zur Verwendung mit irgendeinem Haupt

Methode et appareil pour commander l'enregistrement et la reproduction de voix numerique sur des lignes telephoniques et adapte pour l'usage avec un calculeur

PATENT ASSIGNEE:

VoiceTek Inc, (586901), 61 Chapel Street, Newton Massachusetts 02158, (US), (applicant designated states: AT;BE;CH;DE;FR;GB;IT;LI;LU;NL;SE)

INVENTOR:

Szeto, Charles, 38 Ellis Street, Medway Massachusetts 02053, (US)

LEGAL REPRESENTATIVE:

Allsop, John Rowland (47681), European Patent Attorney, 10 London End, Beaconsfield, Bucks. HP9 2JH, (GB)

PATENT (CC, No, Kind, Date): EP 185445 A2 860625 (Basic)
 EP 185445 A3 880406

APPLICATION (CC, No, Date): EP 85307365 851014;

PRIORITY (CC, No, Date): US 682164 841217

DESIGNATED STATES: AT; BE; CH; DE; FR; GB; IT; LI; LU; NL; SE

INTERNATIONAL PATENT CLASS (V7): H04M-003/50; G06F-003/16;

ABSTRACT WORD COUNT: 247

LANGUAGE (Publication,Procedural,Application): English; English; English
 FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
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CLAIMS A	(English)	EPABF1	746
SPEC A	(English)	EPABF1	10559
Total word count - document A			11305
Total word count - document B			0
Total word count - documents A + B			11305

...SPECIFICATION also take the digital information generated by the telephone interface system 6 and the voice processing system 8 from their parallel busses 5 and 7 and translate that digital information into RS 232-C standard serial data character strings for transmittal to the host computer 2. The ability of the supervisor 3 to perform these tasks allows...

5/3,K/14 (Item 14 from file: 348)
 DIALOG(R)File 348:EUROPEAN PATENTS
 (c) 2010 European Patent Office. All rts. reserv.

00172342
 Display apparatus.
 Anzeigegerat.
 Dispositif d'affichage.
 PATENT ASSIGNEE:

International Business Machines Corporation, (200120), Old Orchard Road,
 Armonk, N.Y. 10504, (US), (applicant designated states: DE;FR;GB;IT)

INVENTOR:

Nojima, Katsuyuki, 1-11 Terrace House 213-35 Sasanodai Asahi-ku,
 Yokohama-shi Kanagawa-ken, (JP)

Nakagawa, Banri, 1772 Kamiwada, Yamato-shi Kanagawa-ken, (JP)

LEGAL REPRESENTATIVE:

Herzog, Friedrich Joachim, Dipl.-Ing. (5411), IBM Deutschland GmbH
 Schonaicher Strasse 220, W-7030 Boblingen, (DE)

PATENT (CC, No, Kind, Date): EP 185294 A2 860625 (Basic)
 EP 185294 A3 890118
 EP 185294 B1 920729

APPLICATION (CC, No, Date): EP 85115700 851210;

PRIORITY (CC, No, Date): JP 84267638 841220

DESIGNATED STATES: DE; FR; GB; IT

INTERNATIONAL PATENT CLASS (V7): G09G-001/16;

ABSTRACT WORD COUNT: 105

LANGUAGE (Publication,Procedural,Application): English; English; English
 FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	1006
CLAIMS B	(German)	EPBBF1	856
CLAIMS B	(French)	EPBBF1	1149

SPEC B	(English)	EPBBF1	3097
Total word count - document A			0
Total word count - document B			6108
Total word count - documents A + B			6108

...SPECIFICATION character generator 30 generates the dot patterns of characters corresponding to character bytes stored in the character register 46, and these patterns are converted to serial data by the parallel-serial converter 32 and transmitted to the video controller 34. The video controller 34 corrects patterns from the converter 32 in accordance with the content of the attribute register 48 and transmits them...

5/3,K/15 (Item 1 from file: 349)
 DIALOG(R)File 349:PCT FULLTEXT
 (c) 2010 WIPO/Thomson. All rts. reserv.

01662826 **Image available**

METHOD AND SYSTEM FOR CONTROLLING A MIXED ARRAY OF POINT-OF-LOAD REGULATORS THROUGH A BUS TRANSLATOR

PROCEDE ET SYSTEME DE COMMANDE D'UN RESEAU MELANGE DE REGULATEURS DE POINT DE CHARGE PAR LE BIAIS D'UN TRANSLATEUR DE BUS

Patent Applicant/Assignee:

POWER-ONE INC, 740 Calle Plano, Camarillo, CA 93012, US, US (Residence), US (Nationality), (For all designated states except: US)

Patent Applicant/Inventor:

GUZ Mikhail, 2023 Kings Lane, San Mateo, CA 94402, US, US (Residence), US (Nationality), (Designated only for: US)

CHAPUIS Alain, Riedikerstrasse 86a, CH-8616 Riedikon, CH, CH (Residence), CH (Nationality), (Designated only for: US)

Legal Representative:

BERLINER Brian M (agent), O'Melveny & Myers LLP, 400 South Hope Street, Los Angeles, CA 90071, US

Patent and Priority Information (Country, Number, Date):

Patent: WO 200861039 A2-A3 20080522 (WO 0861039)

Application: WO 2007US84353 20071109 (PCT/WO US2007084353)

Priority Application: US 2006558848 20061110

Designated States:

(All protection types applied unless otherwise stated - for applications 2004+)

AE AG AL AM AT AU AZ BA BB BG BH BR BW BY BZ CA CH CN CO CR CU CZ DE DK DM DO DZ EC EE EG ES FI GB GD GE GH GM GT HN HR HU ID IL IN IS JP KE KG KM KN KP KR KZ LA LC LK LR LS LT LU LY MA MD ME MG MK MN MW MX MY MZ NA NG NI NO NZ OM PG PH PL PT RO RS RU SC SD SE SG SK SL SM SV SY TJ TM TN TR TT TZ UA UG US VC VN ZA ZM ZW

(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IS IT LT LU LV MC MT

NL PL PT RO SE SI SK TR

(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG

(AP) BW GH GM KE LS MW MZ NA SD SL SZ TZ UG ZM ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 7492

Fulltext Availability:

Detailed Description

Detailed Description

... message communicated from the at least one POL regulator in the second format to the first format compatible with the system controller. The first and second data formats may comprise either a digital data format or an analog data format. The bus translator may further include a phase synchronization circuit adapted to synchronize operation of the bus translator to a detected data rate of the data bus or to synchronize the operation of the POL regulator.

A more...

5/3,K/16 (Item 2 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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01639734

PROCESS FOR THE PRODUCTION OF A FINE CHEMICAL
PROCEDE DE PRODUCTION D'UN PRODUIT CHIMIQUE FIN
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Patent and Priority Information (Country, Number, Date):

Patent: WO 200834648 A1 20080327 (WO 0834648)

Application: WO 2007EP53344 20070404 (PCT/WO EP2007053344)

Priority Application: EP 20061124855 20060405; EP 20061124954 20060407;
EP 20061127379 20060412; EP 20061142105 20060515; EP 20061142733
20060518; EP 20061142527 20060518; EP 20061142584 20060518; EP
20061146775 20060519; EP 20061173944 20060524; EP 20061155248 20060613;
EP 20061154167 20060614; EP 20061167920 20060707; EP 20061167888
20060707; EP 20061168811 20060710; EP 20061175782 20060720; EP
20061179859 20060727; EP 20061180261 20060728; EP 20061181889 20060731;
EP 20061183299 20060802; EP 20061183984 20060803; EP 20061184248
20060804; EP 20061185005 20060807; EP 20061186342 20060809; EP
20061187761 20060811; EP 20061190161 20060816; EP 20061191342 20060818;
EP 20061192118 20060821; EP 20061194486 20060824; EP 20061195129
20060825; EP 20061197471 20060830; EP 20061202560 20060907; EP
20061206348 20060914; EP 20061208989 20060919; EP 20061210977 20060922;
EP 20061212502 20060926; EP 20061213344 20060927; EP 20061214854
20060929; EP 20061215943 20061002; EP 20061216024 20061002; EP
20061230652 20061027; EP 20071000822 20070104; EP 20071001457 20070105;
EP 20071004030 20070111; EP 20071006407 20070117; EP 20071006431
20070117; EP 20071007827 20070119; EP 20071009344 20070122; EP
20071009526 20070123; EP 20071009518 20070123; EP 20071011472 20070125;
EP 20071013288 20070129; EP 20071013304 20070129; EP 20071014542
20070131; EP 20071015366 20070201; EP 20071016158 20070202; EP
20071017370 20070205; EP 20071017123 20070205; EP 20071019350 20070208;
EP 20071019368 20070208; EP 20071021562 20070212; EP 20071022719
20070213; EP 20071027387 20070220; EP 20071028120 20070221; EP
20071030373 20070226; EP 20071051361 20070328

Designated States:

(All protection types applied unless otherwise stated - for applications
2004+)

AE AG AL AM AT AU AZ BA BB BG BH BR BW BY BZ CA CH CN CO CR CU CZ DE DK
DM DZ EC EE EG ES FI GB GD GE GH GM GT HN HR HU ID IL IN IS JP KE KG KM
KN KP KR KZ LA LC LK LR LS LT LU LY MA MD ME MG MK MN MW MX MY MZ NA NG
NI NO NZ OM PG PH PL PT RO RS RU SC SD SE SG SK SL SM SV SY TJ TM TN TR
TT TZ UA UG US VZ VC VN ZA ZM ZW
(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IS IT LT LU LV MC MT
NL PL PT RO SE SI SK TR
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
(AP) BW GH GM KE LS MW MZ NA SD SL SZ TZ UG ZM ZW

(EA) AM AZ BY KG KZ MD RU TJ TM
Publication Language: English
Filing Language: English
Fulltext Word Count: 3258831

Fulltext Availability:
Detailed Description

Detailed Description

... an organism, preferably an increase of methionine between 37% and 55% or more is conferred in said organism. Accordingly, in one embodiment, in case the activity of the A thaliana nucleic acid molecule or a polypeptide comprising the nucleic acid SEQ...

5/3,K/17 (Item 3 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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01545721 **Image available**
INFORMATION REPRODUCING SYSTEM USING INFORMATION STORAGE MEDIUM
SYSTEME DE REPRODUCTION D'INFORMATIONS UTILISANT UN SUPPORT DE STOCKAGE
D'INFORMATIONS

Patent Applicant/Assignee:

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TOYAMA Haruhiko, JP (Residence), JP (Nationality),

Legal Representative:

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Minato-ku Tokyo, 1050001, JP

Patent and Priority Information (Country, Number, Date):

Patent: WO 200788664 A1 20070809 (WO 0788664)
Application: WO 2006JP322916 20061110 (PCT/WO JP2006322916)
Priority Application: JP 200623755 20060131

Designated States:

(All protection types applied unless otherwise stated - for applications
2004+)

AE AG AL AM AT AU AZ BA BB BG BR BW BY BZ CA CH CN CO CR CU CZ DE DK DM
DZ EC EE EG ES FI GB GD GE GH GM GT HN HR HU ID IL IN IS KE KG KM KN KP
KR KZ LA LC LK LR LS LT LU LV LY MA MD MG MK MN MW MX MY MZ NA NG NI NO
NZ OM PG PH PL PT RO RS RU SC SD SE SG SK SL SM SV SY TJ TM TN TR TT TZ
UA UG US UZ VC VN ZA ZM ZW
(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IS IT LT LU LV MC NL

PL PT RO SE SI SK TR

(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG

(AP) BW GH GM KE LS MW MZ NA SD SL SZ TZ UG ZM ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 290851

Fulltext Availability:

Detailed Description

Detailed Description

... new functions. Standard Content basically consists of one VMG space and one or more VTS spaces (which are called as "Standard VTS" or just "VTS"), as shown in FIG. 4 .

In comparison to the existing DVD-Video specification, this embodiment gives...

...video object EVOB that records video information itself, and navigation data IFO that records management data of that EVOB. The standard video title set SVTS includes enhanced video object EVOB that records video information itself and navigation data IFO that records...

5/3,K/18 (Item 4 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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01537571

GENIUS ADAPTIVE DESIGN

MODELE D'ADAPTATION AU GENIE

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(Residence), US (Nationality), (Designated for all)

Patent and Priority Information (Country, Number, Date):

Patent: WO 200781519 A2 20070719 (WO 0781519)

Application: WO 2006US48704 20061219 (PCT/WO US2006048704)

Priority Application: US 2005755291 20051230; US 2006756607 20060105; US 2006778313 20060301; US 2006783018 20060315; US 2006786906 20060328; US 2006852794 20061018

Designated States:

(All protection types applied unless otherwise stated - for applications 2004+)

AE AG AL AM AT AU AZ BA BB BG BR BW BY BZ CA CH CN CO CR CU CZ DE DK DM
DZ EC EE EG ES FI GB GD GE GH GM GT HN HR HU ID IL IN IS JP KE KG KM KN
KP KR KZ LA LC LK LR LS LT LU LV LY MA MD MG MK MN MW MX MY MZ NA NG NI
NO NZ OM PG PH PL PT RO RS RU SC SD SE SG SK SL SM SV SY TJ TM TN TR TT

TZ UA UG US UZ VC VN ZA ZM ZW

(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IS IT LT LU LV MC NL

PL PT RO SE SI SK TR

(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG

(AP) BW GH GM KE LS MW MZ NA SD SL SZ TZ UG ZM ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 520275

Fulltext Availability:

Detailed Description

Detailed Description

... user's voice ("sa"), eg: voice lie detectors. Cheaper systems can simply seek high frequencies representing stress.-sa: "mic"-> "analyzer" (system analyzes person's voice)-> system responds according to PR (software program) / *U's control.-Drawing: Voice = "Sound Analyzer" ("mic" = sound to signal converter) / **M = c (computer) = function (application) / osc . "Coordinates" between different behavioral characteristics (of accessor).-Coordinator-Score: Score: sys coordinates/juggles different Features/files behaviors...

5/3,K/19 (Item 5 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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01313061 **Image available**

METHOD FOR AT LEAST PARTIALLY COMPENSATING FOR ERRORS IN INK DOT PLACEMENT DUE TO ERRONEOUS ROTATIONAL DISPLACEMENT

PROCEDE POUR LA COMPENSATION AU MOINS PARTIELLE D'ERREURS DANS LE PLACEMENT POINTS D'ENCRE DUES A UN DEPLACEMENT ROTATIONNEL ERRONE

Patent Applicant/Assignee:

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Patent Applicant/Inventor:

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JACKSON PULVER Mark, Silverbrook Research Pty Ltd, 393 Darling Street, Balmain, New South Wales 2041, AU, AU (Residence), AU (Nationality), (Designated only for: US)

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WEBB Michael John, Silverbrook Research Pty Ltd, 393 Darling Street, Balmain, New South Wales 2041, AU, AU (Residence), AU (Nationality), (Designated only for: US)

MORPHETT Benjamin David, Silverbrook Research Pty Ltd, 393 Darling Street, Balmain, New South Wales 2041, AU, AU (Residence), AU (Nationality), (Designated only for: US)

Patent and Priority Information (Country, Number, Date):

Patent: WO 2005120835 A1 20051222 (WO 05120835)

Application: WO 2004AU706 20040527 (PCT/WO AU04000706)

Priority Application: WO 2004AU706 20040527

Designated States:

(All protection types applied unless otherwise stated - for applications 2004+)

AE AG AL AM AT AU AZ BA BB BG BR BW BY BZ CA CH CN CO CR CU CZ DE DK DM
DZ EC EE EG ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC
LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NA NI NO NZ OM PG PH PL PT RO
RU SC SD SE SG SK SL SY TJ TM TN TR TT TZ UA UG US UZ VC VN YU ZA ZM ZW
(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LU MC NL PL PT RO
SE SI SK TR
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
(AP) BW GH GM KE LS MW MZ NA SD SL SZ TZ UG ZM ZW
(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 618378

Fulltext Availability:

Detailed Description

Claims

Claim

... the resultant image to the linking printhead.

6 1.2 Embedded CPU

SoPEC contains an embedded CPU for general-purpose system

configuration and management. The CPU performs page and band header

processing, motor control and sensor monitoring (via the GPIO) and other

...million transistors) Low cost

High functionality

900 Million dots per second Extremely fast page generation

>1 0,000 lines-per second at 1600 dpi 0.5 A4/Letter pages per SoPEC

chip per
second
1 chip drives up to 92,160 nozzles Low cost page-width printers
1 chip drives up to 6 color...

...discussed in "Central Processing Unit (CPU)" under Section 11.3 Realtime requirements.

10.2 NORMAL OPERATION IN A SINGLE SoPEC SYSTEM WITH USB HOST CONNECTION

SoPEC operation is broken up into a number of sections which are outlined below. Buffer management...protocols for interfaces implemented with the MMI Image processing fimctions such as image scaling, cropping, rotation, white-balance, color space conversion etc. for printing images directly from digital cameras (e.g. via PictBridge application software)

Miscellaneous housekeeping tasks

To control the...UNIT (MMU)

Memory Management Units are typically used to protect certain regions of memory from invalid accesses, to perform address translation for a virtual memory system and to maintain memory page status (swapped-in, swapped-out or unmapped)

The SoPEC M...of three components: the lCache controller (icache.vhd), the DCache controller (dcache.vhd) and the AHB bridge (acache.vhd) which translates all cache misses into memory requests on the AHB bus. In order to enable full line refill operation a few...2 below describe streaming and non-streaming modes respectively. Each IN or OUT endpoint's buffer in DRAM can be configured to operate as either a circular buffer or a double buffer. Each IN and OUT endpoint has two DMA descriptors...

...status write. If the packet was not received successfully by the USB host, DmaInnStrinPtr is returned to DmalnnCurAdrA and the data is streamed out again if requested by the host. When DmalnnCurAdrA reaches or passes DmalnnIntMrA, an interrupt is generated on...

5/3,K/20 (Item 6 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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01184330 **Image available**

BURST TRANSMISSION

TRANSMISSION PAR RAFALES

Patent Applicant/Assignee:

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Legal Representative:
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Patent and Priority Information (Country, Number, Date):
 Patent: WO 2004107619 A1 20041209 (WO 04107619)
 Application: WO 2004IB50782 20040526 (PCT/WO IB04050782)
 Priority Application: GB 200312439 20030530
Designated States:
 (All protection types applied unless otherwise stated - for applications
 2004+)
 AE AG AL AM AT AU AZ BA BB BG BR BW BY BZ CA CH CN CO CR CU CZ DE DK DM
 DZ EC EE EG ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC
 LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NA NI NO NZ OM PG PH PL PT RO
 RU SC SD SE SG SK SL SY TJ TM TN TR TT TZ UA UG US UZ VC VN YU ZA ZM ZW
 (EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LU MC NL PL PT RO
 SE SI SK TR
 (OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
 (AP) BW GH GM KE LS MW MZ NA SD SL SZ TZ UG ZM ZW
 (EA) AM AZ BY KG KZ MD RU TJ TM
Publication Language: English
Filing Language: English
Fulltext Word Count: 8460

Fulltext Availability:
 Detailed Description
 Claims

Claim

... a burst including a first data structure with a set of time-slicing
 parameters for describing bursts and a second data structure
 without any time-slicing parameters, to identify said first data
 structure; and to extract at least one time
 slicing parameter from said first data structure.

37 a system for transmitting a burst in communications network, the
 system
 comprising:
 providing a first data structure without any time-slicing
 parameters for
 describing bursts;

providing a second data structure with a set of time...

5/3,K/21 (Item 7 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00878683 **Image available**

METHOD AND APPARATUS RELATING TO DATA TRANSPORT
PROCEDE ET APPAREIL CONCERNANT L'ACHEMINEMENT DE DONNEES
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TANG Renjie, 225 Richfield Drive, Apt. #20, San Jose, CA 95129, US,
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MADAPURA Subramanya, 578 Grayson Way, Milipitas, CA 95035, US,

Legal Representative:

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Patent and Priority Information (Country, Number, Date):

Patent: WO 200212839 A2-A3 20020214 (WO 0212839)
Application: WO 2001US25236 20010810 (PCT/WO US0125236)
Priority Application: US 2000637335 20000810

Designated States:

(Protection type is "patent" unless otherwise stated - for applications
prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ
EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR
LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL
TJ TM TR TT TZ UA UG UZ VN YU ZA ZW
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW
(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 5241

Fulltext Availability:

Detailed Description

Detailed Description

... can dedicate one or more threads or processes.

Thereby, a computer having multiple microprocessors can realize its full potential of parallelism in optimizing extraction, transformation, and loading throughput. Furthermore, because the data partitioning structure is userdefined, the user can customize the extent of parallelism desired, thus taking full advantage of the parallel processing capabilities...

5/3,K/22 (Item 8 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00811803 **Image available**

VIDEO, AUDIO AND GRAPHICS DECODE, COMPOSITE AND DISPLAY SYSTEM
SYSTEME COMPOSITE DE PRESENTATION A DECODAGE VIDEO AUDIO ET GRAPHIQUE

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Patent and Priority Information (Country, Number, Date):

Patent: WO 200145426 A1 20010621 (WO 0145426)

Application: WO 2000US33757 20001213 (PCT/WO US0033757)

Priority Application: US 99170866 19991214; US 2000641374 20000818; US
2000641936 20000818; US 2000643223 20000818; US 2000640870 20000818; US

2000640869 20000818; US 2000641930 20000818; US 2000641935 20000818; US
2000642510 20000818; US 2000642458 20000818

Designated States:

(Protection type is "patent" unless otherwise stated - for applications
prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CR CU CZ DE DK DM DZ EE
ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT
LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM
TR TT TZ UA UG US UZ VN YU ZA ZW
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR
((OAPI utility model)) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW
(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 85836

Fulltext Availability:

Detailed Description

Detailed Description

... frames to

memory, and upscales after reading from memory, but preferably
does not perform both upscaling and downscaling at the same
time.

The memory controller 54 preferably reads and writes video
and graphics data to and from memory by using burst accesses with
burst lengths that may be assigned to each task. The memory...

5/3,K/23 (Item 9 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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00802534

ANY-TO-ANY COMPONENT COMPUTING SYSTEM

SYSTEME INFORMATIQUE A COMPOSANTS TOUTE CATEGORIE

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US (Nationality), (Designated only for: US)

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Trace, Suite 300, Marietta, GA 30067, US,
Patent and Priority Information (Country, Number, Date):

Patent: WO 200135216 A2-A3 20010517 (WO 0135216)
Application: WO 2000US31231 20001113 (PCT/WO US0031231)
Priority Application: US 99164884 19991112

Designated States:

(Protection type is "patent" unless otherwise stated - for applications
prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CR CU CZ DE DK DM DZ EE
ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT
LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM
TR TT TZ UA UG US UZ VN YU ZA ZW
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR
(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW
(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 275671

5/3,K/24 (Item 10 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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00546597 **Image available**

SPECTROMETER WITH DUAL DIGITIZER FOR HIGH-DYNAMIC RANGE SPECTROSCOPIC DATA
COLLECTION

SPECTROMETRE AVEC NUMERISEUR POUR COLLECTE DE DONNEE SPECTROSCOPIQUES A
DYNAMIQUE ELEVEE

Patent Applicant/Assignee:

MIDAC CORPORATION,
AUTH Gerald,
HADDAD Roger,
VIDRINE Warren,

Inventor(s):

AUTH Gerald,
HADDAD Roger,
VIDRINE Warren,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200009970 A1 20000224 (WO 0009970)
Application: WO 99US18272 19990810 (PCT/WO US9918272)
Priority Application: US 9895902 19980810

Designated States:

(Protection type is "patent" unless otherwise stated - for applications

prior to 2004)

JP US AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

Publication Language: English

Fulltext Word Count: 6352

English Abstract

A spectrometer (110) having an interferometer, a detector (120) that produces a detector signal (20), and a dual-digitizer system including two analog-to-digital converters that simultaneously digitize low-gain and high-gain versions of the detector signal, and suitable data structures and associated firmware for merging the two resulting sets of digitized data into a single, high dynamic range set of...

5/3,K/25 (Item 11 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

(c) 2010 WIPO/Thomson. All rts. reserv.

00374268

METHOD AND APPARATUS FOR INTERFACING DEVICES USED IN ASYNCHRONOUS COMMUNICATIONS

PROCEDE ET APPAREIL POUR L'INTERFACAGE DE DISPOSITIFS UTILISES EN COMMUNICATION ASYNCHRONE

Patent Applicant/Assignee:

SIERRA SEMICONDUCTOR CORPORATION,

Inventor(s):

RUDDY Paul J,
GODBOLE Vishwas R,
TISINGER Ronald,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9715011 A1 19970424

Application: WO 96US15493 19960927 (PCT/WO US9615493)

Priority Application: US 95544716 19951018

Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

CA JP AT BE CH DE DK ES FI FR GB GR IE IT LU MC NL PT SE

Publication Language: English

Fulltext Word Count: 8047

Fulltext Availability:

Detailed Description

Detailed Description

... format; and means for controlling a transfer of said data between said first device and said second device by selectively converting data of said parallel format into data of said serial format

(e.g., for selective input to or output from an on-board controller) when the first device is in either a serial mode or a parallel mode of operation. Further, the means for controlling selectively converts...

B. Patent Files, Abstract

Patent Literature: Non-Full Text

Dialog files: 347,350

File 347:JAPIO Dec 1976-2010/Jul(Updated 101027)

(c) 2010 JPO & JAPIO

File 350:Derwent WPIX 1963-2010/UD=201069

(c) 2010 Thomson Reuters

Set	Items	Description
S1	4530654	(CONVERT? OR CONVERSION? ? OR TRANSLAT? OR REFORMAT? OR RE-CONFIGUR? OR RESTRUCTUR? OR TRANSFORM? OR TRANSPOS? OR PARSE? ? OR PARSING OR EXTRACT? OR DERIV? OR FILTER?)
S2	117776	S1(5N)(STREAMING OR PARALLEL? OR CONTEMPORANE? OR SYNCHRON? OR (SAME OR ONE)() (TIME OR INSTANT OR MOMENT) OR IMMEDIAT? OR CONCURRENT? OR COINCIDENT? OR SIMULTANE?)
S3	2838	(DATA OR FILE)(1N)(FORMAT? ? OR FORMATT? OR STRUCTUR? OR M-ODEL? OR PARADIGM? OR STANDARD? OR CONFIGUR? OR PATTERN? ? OR ARRANG?)
S4	13293	(FIRST OR ONE OR PRIME OR PRIMARY OR SECOND? OR TWO OR DIF-FER? OR SEPARAT? OR DISTINCT? OR PLURAL? OR MULTIPLE? ? OR MU-LTI OR ANOTHER)(3N)(SYSTEM? ? OR PLATFORM? ? OR COMPUTER? ? OR MACHINE? ? OR EQUIPMENT OR CONTROLLER?)
S5	29	S2(5N)S3(5N)S4

5/3,K/1 (Item 1 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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0020761353 - Drawing available

WPI ACC NO: 2010-J77853/201051

Method for detecting error of parallel power conversion elements, involves finding errors during ignition step of parallel power conversion elements for determining abnormality of power conversion element

Patent Assignee: WIDEOKDAEHAKGYO SANHAKHYEOPRYEOKDAN (WIDE-N)

Inventor: KWON Y; LEE J

Patent Family (1 patents, 1 countries)

Patent Application

Number	Kind	Date	Number	Kind	Date	Update
KR 2010012553	A	20100208	KR 200874016	A	20080729	201051 B

Priority Applications (no., kind, date): KR 200874016 A 20080729

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
KR 2010012553	A	KO	5	2	

Original Publication Data by Authority

Argentina

Assignee name & address:

Original Abstracts:

...the parallel power conversion element and comparative waveform data calculate level it is the method for error detecting of the parallel power conversion element of the power converting system*power conversion system using multiple parallel power conversion elements. Reference waveform data is set up the reference waveform data configuration stage by using the gate drive voltage and current. The comparative waveform data calculate level calculates comparative waveform data by...

Claims:

5/3,K/2 (Item 2 from file: 350)
 DIALOG(R)File 350:Derwent WPIX
 (c) 2010 Thomson Reuters. All rts. reserv.

0017843405 - Drawing available

WPI ACC NO: 2008-G63862/200842

XRPX Acc No: N2008-523127

Memory device for use in e.g. digital camera has data format converter with parallel/serial converter that combines data of channels from memory to channel when memory corresponds to bus width redoubling system

Patent Assignee: CANON KK (CANO)

Inventor: KOSUGI M; YAMAZAKI H

Patent Family (1 patents, 1 countries)

Patent			Application			
Number	Kind	Date	Number	Kind	Date	Update
JP 2008129616	A	20080605	JP 2006309893	A	20061116	200842 B

Priority Applications (no., kind, date): JP 2006309893 A 20061116

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes

Alerting Abstract ...memory controller (16) to memory (14) when memory corresponds to double data rate (DDR) transfer system while has a serial/parallel converter which separates data from memory controller into two channels when memory corresponds to bus width redoubling system. A secondary data-format converter supplies data from memory to memory controller when memory corresponds to DDR transfer system, while has parallel/serial converter which combines data of two channels from memory to one channel when memory corresponds to bus width redoubling system.

Original Publication Data by Authority

Argentina

5/3,K/3 (Item 3 from file: 350)
 DIALOG(R)File 350:Derwent WPIX
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0017744579 - Drawing available
 WPI ACC NO: 2008-F65031/200837
 Machine-implemented method involves presenting event data in other event organization system when event organization systems are incompatible
 Patent Assignee: SAP AG (SSAP)
 Inventor: VOGLER H; VOGLER H K; XIAO Y; ZHANG Q
 Patent Family (4 patents, 39 countries)

Patent		Application					
Number	Kind	Date	Number	Kind	Date	Update	
EP 1923826	A1	20080521	EP 200720535	A	20071019	200837	B
US 20080120158	A1	20080522	US 2006601466	A	20061116	200837	E
CN 101188001	A	20080528	CN 200710186762	A	20071116	200853	E
US 7774224	B2	20100810	US 2006601466	A	20061116	201053	E

Priority Applications (no., kind, date): US 2006601466 A 20061116

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing	Notes
EP 1923826	A1	EN	27	11		

Regional Designated States,Original: AL AT BA BE BG CH CY CZ DE DK EE ES
 FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL PL PT RO RS SE SI SK
 TR

Original Publication Data by Authority

Argentina

Assignee name & address:

Claims:

...incompatible with a plurality of user calendar applications within the organization and provides the first data set to the processor synchronizer in a corresponding incompatible format; transforming, via the processor synchronizer of the EI system, the first event data set to a standardized defined event data format and transforming the first event data set from the standardized event data format to a third data format recognized by...

...within the organization when the plurality of user calendar applications within the organization do not support the standardized defined event data format; storing, via a centralized server database of the EI system, the first event data set as transformed by the processor synchronizer of the EI system; storing, via the centralized server database of the EI system, a second event data set representing...

...at least a subset of the first event data set representing the one or more events as scheduled by the first electronic calendar system as transformed by the processor synchronizer into the standardized event data format or as transformed into the third data format, and further presenting within the same scheduling view, at least a subset of the second event data set representing the...

5/3,K/4 (Item 4 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2010 Thomson Reuters. All rts. reserv.

0014442204 - Drawing available
WPI ACC NO: 2004-632803/200461
XRPX Acc No: N2004-500006
Multiport switch for processing data frame, has set of ingress state machines processing ingress functions based upon portions of frame information and port logic receiving and decoding descriptor from rules checker logic

Patent Assignee: ADVANCED MICRO DEVICES INC (ADMI)

Inventor: MERCHANT S C

Patent Family (1 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update
US 6778547	B1	20040817	US 1999315849	A	19990521	200461 B

Priority Applications (no., kind, date): US 1999315849 A 19990521

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing	Notes
US 6778547	B1	EN	21	7		

Original Publication Data by Authority

Argentina

Assignee name & address:

Claims:

...ingress state machines concurrently processing ingress functions based upon on portions of the header or the frame information; and a plurality of egress state machines configured for generating a descriptor comprising a port vector that selectively specifies which of the ports the data frame is to be transmitted from or whether the data frame is to be filtered; and port logic configured for...

5/3,K/5 (Item 5 from file: 350)
 DIALOG(R)File 350:Derwent WPIX
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0013901907 - Drawing available
 WPI ACC NO: 2004-081335/200408
 Related WPI Acc No: 2004-070452
 XRPX Acc No: N2004-065018

Information system for Internet, has integration agent to create one or more query results by uniting information obtained from different information sources through one of integration agents

Patent Assignee: DEAN C J (DEAN-I)

Inventor: DEAN C J

Patent Family (2 patents, 1 countries)

Patent	Application
Number	Kind Date Update
US 20030233401	A1 20031218 US 2002171916 A 20020614 200408 B
US 7103593	B2 20060905 US 2002171916 A 20020614 200660 E

Priority Applications (no., kind, date): US 2002171916 A 20020614

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing	Notes
US 20030233401	A1	EN	12	3		

Original Publication Data by Authority

Argentina

Assignee name & address:

Claims:

...and transforms said query results, returning said query results to the requesting client of said plurality of client applications, whereby a user or application can **simultaneous** query **multiple** different systems without specific knowledge of each system's query format or data representation.

5/3,K/6 (Item 6 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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0013403243

WPI ACC NO: 2003-493491/200346

XRAM Acc No: C2003-132184

XRPX Acc No: N2003-391988

Creating coherent data set to model biological systems, by storing data linked to unique identifier of biological sample into computer, converting linked data to numeric format that is converted to common unit system data
Patent Assignee: ALLEN K (ALLE-I); BEECHER C (BEEC-I); BOYES D (BOYE-I);

BROADWELL D (BROA-I); COFFIN M (COFF-I); DAVIS K (DAVI-I); GLASSBROOK N (GLAS-I); HAMILTON C (HAMI-I); HOFFMAN N (HOFF-I); HURBAN P (HURB-I); HURBAN P M (HURB-I); ICORIA INC (ICOR-N); LAWRENCE M (LAWR-I); LIDDELL C M (LIDD-I); NYE G J (NYEG-I); PARADIGM GENETICS INC (PARA-N); POPA-BURKE I (POPA-I); RANASINGHE Y (RANA-I); SHUSTER J (SHUS-I); WINFIELD S (WINF-I); WOESSNER J (WOES-I); ZHANG W (ZHAN-I)

Inventor: ALLEN K; BEECHER C; BOYES D; BROADWELL D; COFFIN M; DAVIS K; GLASSBROOK N; HAMILTON C; HOFFMAN N; HURBAN P; HURBAN P M; LAWRENCE M; LIDDELL C; LIDDELL C M; MULPURI R; NYE G; NYE G J; POPA-BURKE I; RANASINGHE Y; SHUSTER J; SLATER T; TANZER M; WINFIELD S; WOESSNER J; ZHANG W

Patent Family (14 patents, 99 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update
WO 2003046798	A1	20030605	WO 2002US37301	A	20021120	200346 B
US 20030229451	A1	20031211	US 2001331948	P	20011121	200382 E
			US 2001344953	P	20011221	

...
Priority Applications (no., kind, date): US 2001331948 P 20011121; US 2001344953 P 20011221; US 2002356994 P 20020214; US 2002363685 P 20020312; US 2002368776 P 20020329; US 2002372679 P 20020415; US 2002374229 P 20020419; US 2002379562 P 20020510; US 2002384445 P 20020530; US 2002404233 P 20020816; US 2002407840 P 20020903; US 2002408721 P 20020906; US 2002414488 P 20020927; US 2002300166 A 20021120; US 2002300184 A 20021120; US 2002300204 A 20021120; US

2002300262 A 20021120; US 2002300291 A 20021120; US 2002300360 A
20021120; US 2002300543 A 20021120; US 2002300551 A 20021120; US
2002300598 A 20021120; US 2002300599 A 20021120

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing	Notes
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WO 2003046798	A1	EN	484	23		
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National Designated States,Original: AE AG AL AM AT AU AZ BA BB BG BR BY
BZ CA CH CN CO CR CU CZ DE DK DM DZ EC EE ES FI GB GD GE GH GM HR HU ID
IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ
NO NZ OM PH PL PT RO RU SD SE SG SI SK SL TJ TM TN TR TT TZ UA UG US UZ
VN YU ZA ZM ZW

Regional Designated States,Original: AT BE BG CH CY CZ DE DK EA EE ES FI
FR GB GH GM GR IE IT KE LS LU MC MW MZ NL OA PT SD SE SK SL SZ TR TZ UG
ZM ZW

US 20030229451	A1	EN			Related to Provisional	US 2001331948
					Related to Provisional	US 2001344953
					Related to Provisional	US 2002356994

Original Publication Data by Authority

Argentina

Assignee name & address:

Claims:

...sample, comprising:a) means for entering a unique identifier of at least one biological sample into a computer tracking system;b) means for simultaneously collecting data from said sample, for a plurality of peaks, each peak comprising at least one chemical component, wherein said data comprise data from at least two processes;c) means for storing said data in said computer tracking system, wherein said data are linked to said unique identifier; andd) means for characterizing and/or identifying said chemical components....

5/3,K/7 (Item 7 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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0013359507 - Drawing available
WPI ACC NO: 2003-447577/200342
XRPX Acc No: N2003-356917

Data files conversion system in computer automated document and file management system, includes multiple data processing devices which are programmed to convert files from various format into common format
Patent Assignee: DZHENIS A (DZIE-I)

Inventor: DZIENIS A

Patent Family (1 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update
US 20030037302	A1	20030220	US 2001300662	P	20010624	200342 B
			US 2002177953	A	20020621	

Priority Applications (no., kind, date): US 2001300662 P 20010624; US 2002177953 A 20020621

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
US 20030037302	A1	EN	9	3	Related to Provisional US 2001300662

Original Publication Data by Authority

Argentina

Assignee name & address:

Original Abstracts:

Systems and methods provide ~~parallel~~ processing for ~~simultaneously converting~~ a plurality of files into various file formats into a ~~common~~ file format. Electronic storage media containing multiple files in various file formats is made accessible to a plurality of personal computers ~~connected~~ through a network. The ~~plurality of computers simultaneously~~ converts the files into a common format for storage.

Claims:

5/3,K/8 (Item 8 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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0013343250 - Drawing available
WPI ACC NO: 2003-430970/200340
XRPX Acc No: N2003-344045

Method, for providing data for root cause analysis, involves transferring data from structured data model into casually oriented data model complemented with information associated with conditional probabilities of at least two objects

Patent Assignee: ABB AB (ALLM); DAHLQUIST E (DAHL-I); MILANOVIC R (MILA-I); VALLMAR G (VALL-I); WEIDL G (WEID-I)

Inventor: DAHLQUIST E; MILANOVIC R; VALLMAR G; WEIDL G

Patent Family (4 patents, 101 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update
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WO 2003042770	A1	20030522	WO 2002EP13900	A	20021115	200340	B
AU 2002352230	A1	20030526	AU 2002352230	A	20021115	200464	E
EP 1481298	A1	20041201	EP 2002787922	A	20021115	200478	E
			WO 2002EP13900	A	20021115		
US 20050049988	A1	20050303	US 2004845519	A	20040514	200517	E

Priority Applications (no., kind, date): GB 200127553 A 20011116

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing	Notes
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WO 2003042770	A1	EN	66	18		
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National Designated States,Original: AE AG AL AM AT AU AZ BA BB BG BR BY
 BZ CA CH CN CO CR CU CZ DE DK DM DZ EC EE ES FI GB GD GE GH GM HR HU ID
 IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ
 NO NZ OM PH PL PT RO RU SC SD SE SG SI SK SL TJ TM TN TR TT TZ UA UG US
 UZ VC VN YU ZA ZM ZW

Regional Designated States,Original: AT BE BG CH CY CZ DE DK EA EE ES FI
 FR GB GH GM GR IE IT KE LS LU MC MW MZ NL OA PT SD SE SK SL SZ TR TZ UG
 ZM ZW

AU 2002352230	A1	EN	Based on OPI patent	WO 2003042770
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EP 1481298	A1	EN	PCT Application	WO 2002EP13900
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			Based on OPI patent	WO 2003042770
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Regional Designated States,Original: AL AT BE BG CH CY CZ DE DK EE ES FI
 FR GB GR IE IT LI LT LU LV MC MK NL PT RO SE SI SK TR

Original Publication Data by Authority

Argentina

Assignee name & address:

Original Abstracts:

...data model into a causally oriented data model. The causally oriented data model is then complemented with information associated with conditional probabilities between at least two objects of the causally oriented data model. This may be accomplished by a translator engine or a computer program product comprising program code means. The causally oriented data models enable simultaneous analysis of at least two root cause hypotheses. The causally oriented data models may also be used for simulation of the impact of an action taken by an operator before any real action...

Claims:

5/3,K/9 (Item 9 from file: 350)
 DIALOG(R)File 350:Derwent WPIX
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0013244117 - Drawing available

WPI ACC NO: 2003-329289/200331

XRPX Acc No: N2003-263424

Ion implantation device parameter monitoring system has computer coupled to ion implantation devices, to convert and process ion implantation process log data as text data, and store graphic data and text data

Patent Assignee: SAMSUNG ELECTRONICS CO LTD (SMSU)

Inventor: KIM J; KIM J P; LEE M; LEE M H

Patent Family (4 patents, 2 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update
US 20030001111	A1	20030102	US 2002185553	A	20020627	200331 B
KR 2003002418	A	20030109	KR 200138017	A	20010629	200333 E
KR 418523	B	20040211	KR 200138017	A	20010629	200438 E
US 7041990	B2	20060509	US 2002185553	A	20020627	200632 E

Priority Applications (no., kind, date): KR 200138017 A 20010629; US 2002185553 A 20020627

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
US 20030001111	A1	EN	9	4	
KR 418523	B	KO			Previously issued patent KR 2003002418

Original Publication Data by Authority

Argentina

Assignee name & address:

Claims:

...graphically process the log data to produce graphic data, the local computer configured to store the text data and the graphic data in a database, and the local computer configured to display the text data and the graphic data using the first monitor.

5/3,K/10 (Item 10 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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0012468236 - Drawing available

WPI ACC NO: 2002-414571/200244

XRPX Acc No: N2002-325991

Panel display system for computer, has display device which adjusts skew between portions of image data signal based on first and second control signals

Patent Assignee: SHIMAMOTO H (SHIM-I); TOSHIBA CORP (TOKE); TOSHIBA KK (TOKE)

Inventor: SHIMAMOTO H

Patent Family (6 patents, 4 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update
US 20020036651	A1	20020328	US 2001955033	A	20010919	200244 B
JP 2002099269	A	20020405	JP 2000291302	A	20000925	200244 E
CN 1347026	A	20020501	CN 2001140842	A	20010921	200252 E
TW 536683	A	20030611	TW 2001123415	A	20010919	200374 E
US 6750856	B2	20040615	US 2001955033	A	20010919	200439 E
CN 1152297	C	20040602	CN 2001140842	A	20010921	200612 E

Priority Applications (no., kind, date): JP 2000291302 A 20000925; US 2001955033 A 20010919

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
US 20020036651	A1	EN	9	3	
JP 2002099269	A	JA	10		
TW 536683	A	ZH			

Original Publication Data by Authority

Argentina

Assignee name & address:

Claims:

...an image data signal that is a parallel signal of plural bits and a control signal concerning horizontal synchronization and **vertical synchronization**; a first transmission device which **converts** part of the **image data** signal into a first serial signal and transmits the first serial signal and a first control signal corresponding to the...

5/3,K/11 (Item 11 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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0012316935 - Drawing available
WPI ACC NO: 2002-258553/200231
XRPX Acc No: N2002-200298
Arrangement for filtering digital data for video systems by controlling bursts of synchronizing data to level which can be processed by encoder circuit in given period
Patent Assignee: KONINK PHILIPS ELECTRONICS NV (PHIG); PHILIPS CORP

INTELLECTUAL PROPERTY GMBH (PHIG); PHILIPS GLOEILAMPENFAB NV (PHIG); US
PHILIPS CORP (PHIG)

Inventor: BERG M; GEHRKE W

Patent Family (4 patents, 28 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update
DE 10002964	A1	20010726	DE 10002964	A	20000125	200231 B
EP 1126696	A2	20010822	EP 2001200147	A	20010116	200231 E
JP 2001285671	A	20011012	JP 200112786	A	20010122	200231 E
US 20010016001	A1	20010823	US 2001766751	A	20010122	200231 E

Priority Applications (no., kind, date): DE 10002964 A 20000125

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
DE 10002964	A1	DE	5	1	
EP 1126696	A2	DE			

Regional Designated States, Original: AL AT BE CH CY DE DK ES FI FR GB GR
IE IT LI LT LU LV MC MK NL PT RO SE SI TR
JP 2001285671 A JA 5

Original Publication Data by Authority

Argentina

Assignee name & address:

Original Abstracts:

...digital data comprising synchronizing information, in which the arrangement operates in a system clock, an orderly data transfer for a rapid synchronization after disturbance of the data is ensured in that the arrangement comprises a first filter (1) and a second, succeeding filter (2) which supplies the output signal of the arrangement, in that the first filter (1) receives at least the synchronizing information comprised in the data and the second filter (2) receives the output signal of the first filter (1) as well as the digital data, in...

Claims:

5/3,K/12 (Item 12 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2010 Thomson Reuters. All rts. reserv.

0010932199 - Drawing available
WPI ACC NO: 2001-554417/200162
XRPX Acc No: N2001-412394

Computer system delivers specific signal group which is influenced directly to operating speed, in synchronization with serial conversion operation

Patent Assignee: SUGITA T (SUGI-I); TOSHIBA KK (TOKE)

Inventor: SUGITA T

Patent Family (3 patents, 2 countries)

Patent			Application			
Number	Kind	Date	Number	Kind	Date	Update
JP 2001236307	A	20010831	JP 200044582	A	20000222	200162 B
US 20010029539	A1	20011011	US 2001788401	A	20010221	200162 E
US 6775731	B2	20040810	US 2001788401	A	20010221	200453 E

Priority Applications (no., kind, date): JP 200044582 A 20000222

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing	Notes
JP 2001236307	A	JA	8	4		

Original Publication Data by Authority

Argentina

Assignee name & address:

Claims:

...unit;a second signal line configured to directly transmit control signals between said main body of said computer and said extension unit;wherein said main body of said computer includes:a generator configured to generate data signals and a first control signal transmitted to said peripheral equipment;a parallel-serial converter configured to convert the data signals into the serial data signals and serially transmit the serial data signals to said extension unit via...

...data signals to make the first control signal bypass said first signal line;wherein said extension unit includes a serial-parallel converter configured to convert the serial data signals into parallel data signals and transmit the parallel data signals to the peripheral equipment.>

5/3,K/13 (Item 13 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2010 Thomson Reuters. All rts. reserv.

0010696975 - Drawing available

WPI ACC NO: 2001-307043/200132

XRPX Acc No: N2001-219644

Fractional decimator has logic circuit to synchronize bit-serial data

streams produced by parallel-to-serial converters using control signal to output a summed signal

Patent Assignee: MOTOROLA INC (MOTI)

Inventor: BAKER J C; OLIVER J P

Patent Family (1 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update
US 6178186	B1	20010123	US 199849624	A	19980327	200132 B

Priority Applications (no., kind, date): US 199849624 A 19980327

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
US 6178186	B1	EN	14	9	

Original Publication Data by Authority

Argentina

Assignee name & address:

Claims:

...portion of the input signal, the delay element to produce a delayed second portion of the input signal;a second ~~parallel~~-to-serial ~~converter~~ configured to receive the ~~delayed~~ second portion of the input signal, the second ~~parallel~~-to-serial ~~converter~~ to produce a second bit-serial data stream;a controller for ~~producing~~ a control signal; anda circuit configured to receive the control signal, the first bit-serial data stream, and the second bit-serial data stream, the circuit using the control signal to synchronize to the first bit-serial data...

5/3,K/14 (Item 14 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2010 Thomson Reuters. All rts. reserv.

0010247627 - Drawing available
WPI ACC NO: 2000-559592/200052
XRPX Acc No: N2000-414159
Dynamically configurable system for transcoding streaming data in a telecommunication infrastructure with computers using different formats to store and communicate between users
Patent Assignee: LUCENT TECHNOLOGIES INC (LUCI); AVAYA TECHNOLOGY CORP (AVAY)

Inventor: BOUIS J D; SHERER J A

Patent Family (5 patents, 28 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update
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EP 1026872	A1	20000809	EP 2000300375	A	20000119	200052	B
CA 2296179	A1	20000729	CA 2296179	A	20000117	200054	E
JP 2000236386	A	20000829	JP 200019736	A	20000128	200056	E
US 6741608	B1	20040525	US 1999239286	A	19990129	200435	E
CA 2296179	C	20050830	CA 2296179	A	20000117	200558	E

Priority Applications (no., kind, date): US 1999239286 A 19990129

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing	Notes
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EP 1026872	A1	EN	15	5		
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Regional Designated States, Original: AL AT BE CH CY DE DK ES FI FR GB GR
IE IT LI LT LU LV MC MK NL PT RO SE SI

CA 2296179	A1	EN				
JP 2000236386	A	JA	9			
CA 2296179	C	EN				

Original Publication Data by Authority

Argentina

Assignee name & address:

Original Abstracts:

...dynamically configurable system and method for transcoding streaming data and a telecommunications infrastructure incorporating the system or the method. In one embodiment, the system includes: (1) first, second and third streaming conversion modules capable of converting data stream portions between internal data formats and (2) a transcoding controller, associated with the first, second and third streaming conversion modules, that determines source and destination data formats of the data stream and arranges ones of the first, second and third streaming conversion modules in an optimal series...

...dynamically configurable system and method for transcoding streaming data and a telecommunications infrastructure incorporating the system or the method. In one embodiment, the system includes: (1) first, second and third streaming conversion modules capable of converting data stream portions between internal data formats and (2) a transcoding controller, associated with the first, second and third streaming conversion modules, that determines source and destination data formats of the data stream and arranges ones of the first, second and third streaming conversion modules in an optimal series...

Claims:

A system for transcoding a data stream, comprising: first, second and third streaming conversion modules capable of converting data

stream portions between internal data formats; anda transcoding controller, associated with said first, second and third streaming conversion modules, that determines source and destination data formats of said data stream and arranges ones of said first, second and third streaming conversion modules in an optimal series...

5/3,K/15 (Item 15 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2010 Thomson Reuters. All rts. reserv.

0009944911 - Drawing available
WPI ACC NO: 2000-246424/200021
XRPX Acc No: N2000-184311
Dual digitizer for high dynamic range spectroscopic data collection
comprises separate digitizers on high and low amplified values of input
Patent Assignee: AUTH G (AUTH-I); HADDAD R (HADD-I); MIDAC CORP (MIDA-N)
; VIDRINE W (VIDR-I)
Inventor: AUTH G; HADDAD R; VIDRINE W
Patent Family (2 patents, 20 countries)
Patent Application
Number Kind Date Number Kind Date Update
WO 2000009970 A1 20000224 WO 1999US18272 A 19990810 200021 B
US 6636319 B1 20031021 US 199895902 P 19980810 200370 E
WO 1999US18272 A 19990810
US 2001762752 A 20010504

Priority Applications (no., kind, date): US 199895902 P 19980810; US
2001762752 A 20010504

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
WO 2000009970	A1	EN	32	5	
National Designated States,Original: JP US					
Regional Designated States,Original: AT BE CH CY DE DK ES FI FR GB GR IE					
IT LU MC NL PT SE					
US 6636319	B1	EN			
					Related to Provisional US 199895902
					PCT Application WO 1999US18272
					Based on OPI patent WO 2000009970

Original Publication Data by Authority

Argentina

Assignee name & address:
Original Abstracts:

...interferometer, a detector (120) that produces a detector signal (20), and a dual-digitizer system including two analog-to-digital converters that simultaneously digitize low-gain and high-gain versions of the detector signal, and suitable data structures and associated firmware for merging the two resulting sets of digitized data into a single, high dynamic range set of...

...interferometer, a detector (120) that produces a detector signal (20), and a dual-digitizer system including two analog-to-digital converters that simultaneously digitize low-gain and high-gain versions of the detector signal, and suitable data structures and associated firmware for merging the two resulting sets of digitized data into a single, high dynamic range set of...

Claims:

5/3,K/16 (Item 16 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2010 Thomson Reuters. All rts. reserv.

0009548045 - Drawing available
WPI ACC NO: 1999-493633/199941
Related WPI Acc No: 2005-210144
XRPX Acc No: N1999-367718
Integrated chip for data flow processor used in neuronal networks
Patent Assignee: PACT GMBH (PACT-N)
Inventor: MUENCH R M; VORBACH M A
Patent Family (1 patents, 1 countries)
Patent Application
Number Kind Date Number Kind Date Update
US 5943242 A 19990824 US 1995544435 A 19951117 199941 B

Priority Applications (no., kind, date): US 1995544435 A 19951117

Patent Details
Number Kind Lan Pg Dwg Filing Notes
US 5943242 A EN 23 22

Original Publication Data by Authority

Argentina

Assignee name & address:

Claims:

...input and output ports;a timer arrangement coupled to at least one of the functional parts and including at least one of a state machine and a counter, the timer arrangement

synchronizing data processing by the at least one of the functional parts and generating synchronization signals; anda compiler configuring and reconfiguring selected ones of the at least some of said cells and selectively grouping and regrouping said selected ones of the...

5/3,K/17 (Item 17 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2010 Thomson Reuters. All rts. reserv.

0009403169 - Drawing available
WPI ACC NO: 1999-339586/199929
XRPX Acc No: N1999-254650
Voice control system for navigation e.g. for vehicles
Patent Assignee: MATSUSHITA DENKI SANGYO KK (MATU); MATSUSHITA ELECTRIC
IND CO LTD (MATU)

Inventor: TSUBOTA K; YAMAMOTO E
Patent Family (4 patents, 27 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update
EP 921641	A2	19990609	EP 1998122984	A	19981203	199929 B
JP 11168388	A	19990622	JP 1997334671	A	19971204	199935 E
US 6230136	B1	20010508	US 1998203818	A	19981202	200128 E
JP 3345327	B2	20021118	JP 1997334671	A	19971204	200279 E

Priority Applications (no., kind, date): JP 1997334671 A 19971204

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
EP 921641	A2	EN	41	27	
Regional Designated States,Original: AL AT BE CH CY DE DK ES FI FR GB GR IE IT LI LT LU LV MC MK NL PT RO SE SI					
JP 11168388	A	JA	12		
JP 3345327	B2	JA	12		Previously issued patent JP 11168388

Original Publication Data by Authority

Argentina

Assignee name & address:

Claims:

...data; basic synchronizing signal selection means for selecting a first basic synchronizing signal required for decoding the voice data coded in the coding system on the specific standard or a second basic synchronizing signal required for decoding voice

data coded in a coding system on another standard different from the coding system on the specific standard in response to the input voice data; and frequency division means for dividing the basic synchronizing signal output from said basic synchronizing signal selection means to generate synchronizing signals to be given to said count means, said data synchronization means, and said parallel/serial data conversion means.

5/3,K/18 (Item 18 from file: 350)
 DIALOG(R)File 350:Derwent WPIX
 (c) 2010 Thomson Reuters. All rts. reserv.

0009314774 - Drawing available
 WPI ACC NO: 1999-245898/199921
 XRPX Acc No: N1999-183122

Transferring data between hosts
 Patent Assignee: HITACHI LTD (HITA)
 Inventor: HONMA S; KITAMURA M; URABE K; URATANI I; YAMAMOTO A
 Patent Family (5 patents, 26 countries)

Patent			Application			
Number	Kind	Date	Number	Kind	Date	Update
EP 903668	A1	19990324	EP 1998116803	A	19980904	199921 B
JP 11085655	A	19990330	JP 1997250252	A	19970916	199923 E
US 6499056	B1	20021224	US 1998151583	A	19980911	200303 E
EP 903668	B1	20030319	EP 1998116803	A	19980904	200325 E
DE 69812257	E	20030424	DE 69812257	A	19980904	200335 E
			EP 1998116803	A	19980904	

Priority Applications (no., kind, date): JP 1997250252 A 19970916; EP 1998116803 A 19980904

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing	Notes
EP 903668	A1	EN	27	14		

Regional Designated States,Original: AL AT BE CH CY DE DK ES FI FR GB GR IE IT LI LT LU LV MC MK NL PT RO SE SI

JP 11085655	A	JA	21
EP 903668	B1	EN	

Regional Designated States,Original: DE FR GB

DE 69812257	E	DE	Application	EP 1998116803
			Based on OPI patent	EP 903668

Alerting Abstract ...host computer. The data location is written to the storage subsystem by the host computer, is read out by the second computer and the operations are conducted in parallel. The OS

converts an access request of a count key data format
issued from an application program toward the storage subsystem into an
access request of a fixed length block format.

Original Publication Data by Authority

Argentina

5/3,K/19 (Item 19 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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0009183386

WPI ACC NO: 1999-107351/199910

XRAM Acc No: C1999-032240

XRPX Acc No: N1999-077569

Electrical textile machine control esp. for needles of the textile machine
- has serial/parallel converters working with pattern data blocks and an
electronic processing unit

Patent Assignee: SIEMENS AG (SIEI)

Inventor: BURGER W; STUEWE H

Patent Family (6 patents, 3 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update
DE 19748293	A1	19990128	DE 19748293	A	19971031	199910 B
WO 1999005350	A1	19990204	WO 1998DE1998	A	19980716	199912 E
DE 19881024	T	20000615	DE 19881024	A	19980716	200036 E
			WO 1998DE1998	A	19980716	
CN 1265164	A	20000830	CN 1998807536	A	19980716	200059 E
US 6269282	B1	20010731	US 2000490325	A	20000124	200146 E
CN 1080781	C	20020313	CN 1998807536	A	19980716	200516 E

Priority Applications (no., kind, date): DE 19731692 A 19970723; DE
19748293 A 19971031

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
DE 19748293	A1	DE	13	5	
WO 1999005350	A1	DE			
National Designated States,Original: CN DE US					
DE 19881024	T	DE			PCT Application WO 1998DE1998
					Based on OPI patent WO 1999005350

Original Publication Data by Authority

Argentina

Assignee name & address:

Original Abstracts:

...Each of the serial-parallel converters is connected to at least one group of mechanical actuation elements of a textile machine. Further, the plurality of serial-parallel converters is connected sequentially to the serial data bus. The electronic processing unit forms pattern data blocks from textile pattern data and transmits the pattern data blocks to the serial-parallel converters. The pattern data blocks...

Claims:

...configured to accept, for each connected group of mechanical actuation elements, at least one pattern data block from the textile pattern data; a serial data bus to which the serial-parallel converters are sequentially connected; an electronic processing unit, having a first connection to said data bus, and which in transmission cycles forms, from the textile pattern data, pattern data chains containing pattern data blocks in such a way that one pattern data chain per transmission cycle contains pattern data...

5/3,K/20 (Item 20 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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0008699628 - Drawing available

WPI ACC NO: 1998-239418/199821

XRPX Acc No: N1998-189371

Conversion system for computer data to video field - receives and converts computer data format scan groups into NTSC format video fields by converting data words of each scan group into data bytes and synchronising bytes with timing requirements of video tape recorder

Patent Assignee: STORAGE TECHNOLOGY CORP (STOS)

Inventor: CHIKIRA M

Patent Family (1 patents, 1 countries)

Patent Application

Number	Kind	Date	Number	Kind	Date	Update
US 5737024	A	19980407	US 1995409153	A	19950323	199821 B

Priority Applications (no., kind, date): US 1995409153 A 19950323

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
US 5737024	A	EN	12	5	

Original Publication Data by Authority

Argentina

Assignee name & address:

Claims:

A system for converting user data between computer data format scan groups and National Television Systems Committee format video fields, comprising: first receiving means for receiving scan groups and for converting said scan groups from computer data format to National Television Systems Committee format video fields, said first receiving means including first converting means for converting data words of each scan group into data bytes, and first synchronizing means for synchronizing said data bytes with timing requirements of a video tape recorder; and second receiving means for receiving...

...Committee format video fields and for converting said National Television Systems Committee format video fields to said scan groups in Computer data format, said second receiving means including second converting means for converting data bytes of each video field into data words; and second synchronizing means for synchronizing said data words with timing requirements of a page buffer memory.

5/3,K/21 (Item 21 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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0008488298 - Drawing available
WPI ACC NO: 1998-017885/199802
Related WPI Acc No: 1996-128899
XRPX Acc No: N1998-013688
Digital audio signal record and playback apparatus e.g. for magneto-optical disk - records onto or playback from disk eight channels of digital audio data with format converted to parallel data and provided to buffer via direct memory access controller with data recorded onto disk via small computer system interface
Patent Assignee: SONY CORP (SONY); SONY ELECTRONICS INC (SONY)
Inventor: SCHMIDT D C; STEVENS S; ZAMPINI M A
Patent Family (1 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update
US 5691966	A	19971125	US 1993128717	A	19930930	199802 B
			US 1995542705	A	19951013	

Priority Applications (no., kind, date): US 1993128717 A 19930930; US

1995542705 A 19951013

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
US 5691966	A	EN	13	5	Continuation of application US
1993128717					Continuation of patent US 5493547

Original Publication Data by Authority

Argentina

Assignee name & address:

Original Abstracts:

...eight channels of digital audio data. Eight channels of serial audio data, for example in a AES/EBU format is converted to parallel data and provided to one or more buffers via a DMA controller. After buffering, the data may be recorded onto a MO disk via an SCSI.

Claims:

5/3,K/22 (Item 22 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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0008420740 - Drawing available

WPI ACC NO: 1997-538803/199750

XRPX Acc No: N1997-448410

Programming system for programmable logic device - has receiving unit that converts serial program data string to parallel data byte which is then provided to programming pin of the programmable logic device

Patent Assignee: LATTICE SEMICONDUCTOR CORP (LATT-N)

Inventor: DEMING A S; GARDNER D T; LARSEN J S; LEIGH B

Patent Family (3 patents, 4 countries)

Patent			Application			
Number	Kind	Date	Number	Kind	Date	Update
EP 806737	A2	19971112	EP 1997303091	A	19970506	199750 B
JP 10083296	A	19980331	JP 1997116720	A	19970507	199823 E
US 5864486	A	19990126	US 1996643291	A	19960508	199911 E

Priority Applications (no., kind, date): US 1996643291 A 19960508

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
EP 806737	A2	EN	16	8	

Regional Designated States,Original: DE GB

Original Publication Data by Authority

Argentina

Assignee name & address:

Original Abstracts:

Program data generated by the host system to be used in programming one or more associated ISPLDs is converted using a first interface from a parallel data format to a serial data string and then transmitted serially to a second interface. In one embodiment, the first and second interfaces employ Universal Asynchronous...

...Program data generated by the host system to be used in programming one or more associated ISPLDs is converted using a first interface from a parallel data format to a serial data string and then transmitted serially to a second interface. In one embodiment, the first and second...

Claims:

5/3,K/23 (Item 23 from file: 350)
 DIALOG(R)File 350:Derwent WPIX
 (c) 2010 Thomson Reuters. All rts. reserv.

0008244261 - Drawing available

WPI ACC NO: 1997-351224/199732

XRPX Acc No: N1997-291029

Adaptor for adapting fibre channel transmissions to industry standard data bus - uses interface logic coupled between industry standard bus and local bus for adapting transmission between host computer and fibre channel

Patent Assignee: UNISYS CORP (BURS)

Inventor: NOGALES C E; SOOY W G

Patent Family (2 patents, 20 countries)

Patent Application

Number	Kind	Date	Number	Kind	Date	Update
WO 1997023832	A1	19970703	WO 1996US20232	A	19961220	199732 B
US 5809328	A	19980915	US 1995576113	A	19951221	199844 E

Priority Applications (no., kind, date): US 1995576113 A 19951221

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing	Notes
WO 1997023832	A1	EN	40	4		

National Designated States,Original: CA JP

Regional Designated States,Original: AT BE CH DE DK ES FI FR GB GR IE IT
LU MC NL PT SE

Original Publication Data by Authority

Argentina

Assignee name & address:

Claims:

...a second input/output terminal;e. a gigabit link module disposed for converting said header and data structures from a parallel format to a serial format and having a first input/output terminal coupled to said second input/output terminal of said fibre channel controller and a second input/output terminal coupled to said fibre channel;f. a microprocessor disposed for providing service requests from said host to read...

5/3,K/24 (Item 24 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2010 Thomson Reuters. All rts. reserv.

0006964495 - Drawing available

WPI ACC NO: 1994-001524/199401

XRFX Acc No: N1994-001184

Signalling protocol on derived data link monitoring system - interfaces data link signalling source and manipulates data generation information.

Patent Assignee: TELEFONAKTIEBOLAGET ERICSSON L M (TELF)

Inventor: DINH B; NAKAR J K

Patent Family (5 patents, 4 countries)

Patent

Number	Kind	Date	Application Number	Kind	Date	Update
EP 576410	A2	19931229	EP 1993850126	A	19930611	199401 B
EP 576410	A3	19940223	EP 1993850126	A	19930611	199519 E
EP 576410	B1	20000308	EP 1993850126	A	19930611	200017 E
DE 69327987	E	20000413	DE 69327987	A	19930611	200025 E
			EP 1993850126	A	19930611	
ES 2145039	T3	20000701	EP 1993850126	A	19930611	200036 E

Priority Applications (no., kind, date): US 1992904163 A 19920625

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
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EP 576410	A2	EN	14	6	
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Regional Designated States,Original: DE ES FR GB IT SE

EP 576410	A3	EN			
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EP 576410	B1	EN			
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Regional Designated States,Original: DE ES FR GB IT SE
 DE 69327987 E DE Application EP 1993850126
 Based on OPI patent EP 576410
 ES 2145039 T3 ES Application EP 1993850126
 Based on OPI patent EP 576410

Original Publication Data by Authority

Argentina

Assignee name & address:

Claims:

...which each data message is associated with indicia relating to the transmissions over said communication link (6) and contains a first predetermined number of **synchronization** bits followed by a second predetermined number of information bits arranged in a plurality of fields, said **system** being connected between an originating terminal (2 or 4) and a receiving terminal (2 or 4), and characterized in that...

5/3,K/25 (Item 25 from file: 350)
 DIALOG(R)File 350:Derwent WPIX
 (c) 2010 Thomson Reuters. All rts. reserv.

0006354406 - Drawing available
 WPI ACC NO: 1993-152027/199318
 XRPX Acc No: N1993-116402
 Conversion system between SCSI data and serial data having multiplexer and two latches - comprises two identical interfaces respectively connected to computer system interface and memory controller, and each having parallel-serial and serial-parallel converters
 Patent Assignee: SAND TECHNOLOGY SYSTEMS DEV INC (SAND-N); SAND TECHNOLOGY SYSTEMS INT INC (SAND-N)

Inventor: BRUNK J L

Patent Family (4 patents, 19 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update
US 5206946	A	19930427	US 1989427723	A	19891027	199318 B
WO 1994012926	A1	19940609	WO 1992US9521	A	19921202	199424 NCE
EP 672274	A1	19950920	WO 1992US9521	A	19921202	199542 NCE
			EP 1993912061	A	19921202	
JP 8505966	W	19960625	WO 1992US9521	A	19921202	199648 NCE
			JP 1994513065	A	19921202	

Priority Applications (no., kind, date): US 1989427723 A 19891027; WO 1992US9521 A 19921202; EP 1993912061 A 19921202; JP 1994513065 A

19921202

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing	Notes
US 5206946	A	EN	11			
WO 1994012926	A1	EN	33			
National Designated States,Original: CA JP						
Regional Designated States,Original: AT BE CH DE DK ES FR GB GR IE IT LU						
MC NL PT SE						
EP 672274	A1	EN	11	5	PCT Application	WO 1992US9521
					Based on OPI patent	WO 1994012926
Regional Designated States,Original: DE FR GB						
JP 8505966	W	JA	21		PCT Application	WO 1992US9521
					Based on OPI patent	WO 1994012926

Original Publication Data by Authority

Argentina

Assignee name & address:

Original Abstracts:

...the multiple computers and a memory controller connected to the memory. First and second identical interfaces are respectively connected in data communication with the computer system interface and the memory controller. Each interface includes a circuit for converting parallel data to serial data format and a circuit for converting serial data to parallel data format. The parallel to serial converting circuit and the serial...

Claims:

5/3,K/26 (Item 26 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2010 Thomson Reuters. All rts. reserv.

0006274907 - Drawing available
WPI ACC NO: 1993-067437/199308
XRPX Acc No: N1993-051680
Storage control system for real-time data acquisition appts. - converts serially received digital data to parallel format and then provides it to storage interface, which is controlled by user input instructions
Patent Assignee: NASA US NAT AERO & SPACE ADMIN (USAS)
Inventor: GRAY D L; WRIGHT K D
Patent Family (2 patents, 1 countries)
Patent Application
Number Kind Date Number Kind Date Update
US N7799571 N 19921215 US 1991799571 A 19911127 199308 B

US 5375221 A 19941220 US 1991799571 A 19911127 199505 E

Priority Applications (no., kind, date): US 1991799571 A 19911127

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
US N7799571	N	EN	110	10	
US 5375221	A	EN	84		

Original Publication Data by Authority

Argentina

Assignee name & address:

Original Abstracts:

...a storage medium to store data obtained by a real-time data acquisition system. Digital data received in serial format from the data acquisition system is first converted to a parallel format and then provided to the storage interface. The operation of the storage interface is controlled in accordance with instructions based...

Claims:

5/3,K/27 (Item 27 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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0005937622 - Drawing available

WPI ACC NO: 1992-168751/199221

XRPX Acc No: N1992-127185

Video display system for e.g. PC work station - selectively controls by window, number of overlay planes, palettes and overlay-underlay plane masks in graphics system

Patent Assignee: IBM DEUT GMBH (IBMC); INT BUSINESS MACHINES CORP (IBMC)

Inventor: BEATTIE I; BEATTIE I L; DESAI N; DESAI N M; VANOVER M; VANOVER M T; VOLTIN J; VOLTIN J A

Patent Family (11 patents, 13 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update
EP 486155	A1	19920520	EP 1991309408	A	19911014	199221 B
WO 1992009066	A1	19920529	WO 1991EP2042	A	19911029	199224 E
CA 2053988	A	19920516	CA 2053988	A	19911022	199231 E
ZA 199108300	A	19920729	ZA 19918300	A	19911017	199236 E
SK 199300464	A3	19930909	SK 1993464	A	19930512	199419 E
CZ 199300900	A3	19940413	CZ 1993900	A	19911029	199422 E
HU 65611	T	19940728	WO 1991EP2042	A	19911029	199431 E
			HU 19931262	A	19911029	

US 5386505	A	19950131	US 1990614350	A	19901115	199511	E
			US 1993161210	A	19931130		
EP 486155	B1	19950426	EP 1991309408	A	19911014	199521	E
DE 69109241	E	19950601	DE 69109241	A	19911014	199527	E
			EP 1991309408	A	19911014		
CA 2053988	C	19951212	CA 2053988	A	19911022	199611	E

Priority Applications (no., kind, date): US 1990614350 A 19901115; WO 1991EP2042 A 19911029; US 1993161210 A 19931130

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing	Notes
EP 486155	A1	EN	17	5		
Regional Designated States,Original: DE FR GB IT						
WO 1992009066	A1	EN	28			
National Designated States,Original: CS DE HU PL SU						
CA 2053988	A	EN				
ZA 199108300	A	EN	25			
HU 65611	T	HU				PCT Application WO 1991EP2042
						Based on OPI patent WO 1992009066
US 5386505	A	EN	11	5		Continuation of application US
1990614350						
EP 486155	B1	EN	18	5		
Regional Designated States,Original: DE FR GB IT						
DE 69109241	E	DE				Application EP 1991309408
						Based on OPI patent EP 486155
CA 2053988	C	EN				

Original Publication Data by Authority

Argentina

Assignee name & address:

Original Abstracts:

...number of overlay planes, the number of overlay palettes, and the overlay/underlay plane masks in a graphics video display system. A logic/multiplex control translates overlay and underlay data patterns from a multiple plane VRAM, referenced to the graphics system frame buffer, into window specific patterns. The window related translation is conveyed to conventional RAMDACs for raster scan synchronized digital-to-analog conversion. The translation as provided by the controller is responsive to data selectively and dynamically written into...

Claims:

5/3,K/28 (Item 28 from file: 350)

DIALOG(R)File 350:Derwent WPIX
(c) 2010 Thomson Reuters. All rts. reserv.

0005481028 - Drawing available

WPI ACC NO: 1991-082703/199112

XRPX Acc No: N1991-063903

Asynchronous time division multiplexed switched system - has converters arranged so packets form queues within memory, and discards longest queue to make room for incoming data when memory is full

Patent Assignee: GEC PLESSEY TELECOM LTD (ENGE); GPT LTD (ENGE); PLESSEY CO LTD (PLES); PLESSEY CO PLC (PLES)

Inventor: TURNER A J

Patent Family (11 patents, 22 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update
GB 2236035	A	19910320	GB 198920980	A	19890915	199112 B
WO 1991004624	A	19910404	WO 1990GB1369	A	19900905	199116 E
AU 199064239	A	19910418				199129 E
EP 454797	A	19911106	EP 1990914221	A	19900905	199145 E
CN 1051645	A	19910522				199207 E
PT 95308	A	19920529	PT 95308	A	19900913	199227 E
GB 2236035	B	19931117	GB 198920980	A	19890915	199346 E
EP 454797	B1	19950301	EP 1990914221	A	19900905	199513 E
			WO 1990GB1369	A	19900905	
DE 69017430	E	19950406	DE 69017430	A	19900905	199519 E
			EP 1990914221	A	19900905	
			WO 1990GB1369	A	19900905	
ES 2068398	T3	19950416	EP 1990914221	A	19900905	199522 E
US 5475680	A	19951212	US 1991689813	A	19910718	199604 E
			US 1993115904	A	19930901	

Priority Applications (no., kind, date): GB 198920980 A 19890915

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
WO 1991004624	A	EN			
National Designated States,Original:					AU BG CA FI JP KR SU US
Regional Designated States,Original:					AT BE CH DE DK ES FR GB IT LU NL SE
EP 454797	A	EN			
Regional Designated States,Original:					BE DE ES FR IT LU NL SE
GB 2236035	B	EN	2	1	
EP 454797	B1	EN	24		
					PCT Application WO 1990GB1369
					Based on OPI patent WO 1991004624
Regional Designated States,Original:					BE DE DK ES FR IT LU NL SE
DE 69017430	E	DE			
					Application EP 1990914221
					PCT Application WO 1990GB1369
					Based on OPI patent EP 454797

ES 2068398 T3 ES

Based on OPI patent WO 1991004624

Application EP 1990914221

US 5475680 A EN 20
1991689813

Based on OPI patent EP 454797

Continuation of application US

Original Publication Data by Authority

Argentina

Assignee name & address:

Claims:

...1. An asynchronous time division **multiplex switching system** comprising a serial to **parallel converter** (100) arranged to receive **incoming packets of data** which include routing information in serial form from input ports (107) and to convert them into parallel form, a memory...

...An asynchronous time division **multiplex switching system** comprising a serial to **parallel converter** arranged to receive incoming packets of **data which include routing** information in serial form and **convert** them into **parallel form**, a memory in **which** each incoming **packet** of data is entered in an addressed location thereof, a processor to arrange said incoming packets of data in queues...

5/3,K/29 (Item 29 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2010 Thomson Reuters. All rts. reserv.

0001863326

WPI ACC NO: 1979-K9984B/197947

Computer digital data exchange circuit - uses data compression via parallel-sequential code conversion for data exchange between different speed systems

Patent Assignee: GALUZA A S (GALU-I)

Inventor: GALUZA A S; YARKOVA T A

Patent Family (1 patents, 1 countries)

Patent Application

Number	Kind	Date	Number	Kind	Date	Update
SU 651333	A	19790308	SU 2440109	A	19770103	197947 B

Priority Applications (no., kind, date): SU 2440109 A 19770103

Alerting Abstract ...circuit between computers and peripherals is attained by using modulo-two adder, synchronisation register, signal length

meter, selector, sequential into parallel code converter and counting triggers...

...The circuit contains transmitter (1), receiver (2), synchroniser (3), parallel-into-sequential code converter (4), synchronisation register (5), encoder (6), AND-gates (7, 8, 17, 18), modulo two adders (9-12), communication lines (13, 14), OR-gate (15), signals length discriminator (16), triggers (19, 20), divider (21), selector (22) and sequential into parallel code converter (24-29...

...The arrangement allows data exchange between different speed systems by data compression through parallel-sequential code conversion allowing two instead conventional four commutation lines to be deployed for simultaneous transmission and reception.

Original Publication Data by Authority

Argentina

IV. Text Search Results from Dialog

A. NPL Files, Abstract

Non-Patent Literature: Non-Full Text

Dialog files: 2,35,65,95,99,256,474,475,583

File 2:INSPEC 1898-2010/Oct W4
(c) 2010 The IET
File 35:Dissertation Abs Online 1861-2010/Sep
(c) 2010 ProQuest Info&Learning
File 65:Inside Conferences 1993-2010/Nov 01
(c) 2010 BLDSC all rts. reserv.
File 95:TEME-Technology & Management 1989-2010/Sep W3
(c) 2010 FIZ TECHNIK
File 99:Wilson Appl. Sci & Tech Abs 1983-2010/Aug
(c) 2010 The HW Wilson Co.
File 256:TecTrends 1982-2010/Oct W3
(c) 2010 Info.Sources Inc. All rights res.
File 474:New York Times Abs 1969-2010/Oct 31
(c) 2010 The New York Times
File 475:Wall Street Journal Abs 1973-2010/Nov 01
(c) 2010 The New York Times
File 583:Gale Group Globalbase(TM) 1986-2002/Dec 13
(c) 2002 Gale/Cengage

Set	Items	Description
S1	3988576	(CONVERT? OR CONVERSION? ? OR TRANSLAT? OR REFORMAT? OR RE- CONFIGUR? OR RESTRUCTUR? OR TRANSFORM? OR TRANSPOS? OR PARSE? ? OR PARSING OR EXTRACT? OR DERIV? OR FILTER?)
S2	68338	S1(5N)(STREAMING OR PARALLEL? OR CONTEMPORANE? OR SYNCHRON? OR (SAME OR ONE)() (TIME OR INSTANT OR MOMENT) OR IMMEDIAT? OR CONCURRENT? OR COINCIDENT? OR SIMULTANE?)
S3	861	(DATA OR FILE)(1N)(FORMAT? ? OR FORMATT? OR STRUCTUR?)
S4	4468	(FIRST OR ONE OR PRIME OR PRIMARY OR SECOND? OR TWO OR DIF- FER? OR SEPARAT? OR DISTINCT? OR PLURAL? OR MULTIPLE? ? OR MU- LTI OR ANOTHER)(2N)(SYSTEM? ? OR PLATFORM? ? OR COMPUTER? ?)
S5	11	S2(S)S3(S)S4
S6	5	S5 NOT PY>2004

6/3,K/1 (Item 1 from file: 2)
DIALOG(R)File 2:INSPEC
(c) 2010 The IET. All rts. reserv.

07621927

Title: A checkpointing strategy for scalable recovery on distributed parallel systems

Author(s): Naik, V.K. 1; Midkiff, S.P. 1; Moreira, J.E. 1

Affiliation(s):

1. IBM Thomas J. Watson Res. Center, Yorktown Heights, NY, USA

Book Title: Proceedings of the 1997 ACM/IEEE SC97 Conference

Inclusive Page Numbers: 24 pp.

Publisher: ACM, New York, NY

Country of Publication: USA

Publication Date: 1997

Conference Title: Proceedings of Supercomputing '97

Conference Date: 15-21 Nov. 1997

Conference Location: San Jose, CA, USA

Conference Sponsor: ACM IEEE Comput. Soc

ISBN: 0-89791-985-8

U.S. Copyright Clearance Center Code: 0 89791 985 8/97/0011\$3.50

Language: English

Subfile(s): C (Computing & Control Engineering)

INSPEC Update Issue: 2000-024

Copyright: 2000, IEE

Abstract: ...systems. The novelty of our scheme is that a checkpointed application can be restored, from its checkpointed state, in a reconfigured form. Thus, a parallel application may be checkpointed while executing with t_1 tasks on p_1 processors, and then restarted from the checkpointed state with...

...result, applications can recover from partial failures in the underlying system. Also, the reconfigurable checkpointed states can be migrated from one parallel system to another even if they do not have the same number of processors. We describe a new programming model for implementing a reconfigurable checkpointing scheme for parallel programs. This new model is derived from the DRMS programming model, developed in the context of run-time reconfiguration of parallel applications. A key component of our implementation is the distribution independent representation of application array data structures in persistent storage. For further optimizing the performance of checkpoint/restart operations, we provide parallel array section streaming operations for such distributed arrays. We present performance data for the reconfigurable checkpointing and restarting of parallel applications and compare that with the performance of conventional forms of checkpointing. Our results demonstrate the advantages of the new...

6/3,K/2 (Item 2 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2010 The IET. All rts. reserv.

06944909

Title: SOP: an adaptive massively parallel computer and its control-data-flow based compiling method
Author(s): Yamauchi, T.; Nakaya, S.; Kajihara, N.
Book Title: Parcella '96. Proceedings of the VII. International Workshop on Parallel Processing by Cellular Automata and Arrays
Inclusive Page Numbers: 128-36
Publisher: Akademie Verlag, Berlin
Country of Publication: Germany
Publication Date: 1996
Conference Title: Proceedings of Seventh International Workshop on Parallel Processing by Cellular Automata and Arrays
Conference Date: 16-20 Sept. 1996
Conference Location: Berlin, Germany
Editor(s): Vollmar, R.; Erhard, W.; Jossifov, V.
ISBN: 3-05-501750-1
Number of Pages: 341
Language: English
Subfile(s): C (Computing & Control Engineering)
INSPEC Update Issue: 1998-024
Copyright: 1998, IEE

Abstract: ...performance by mapping the control and data flow of target algorithms directly on to a reconfigurable hardware. To realize this **system**, two items should be carefully considered: 1) a hardware architecture, 2) a mapping method. In this paper, we present fundamental architecture...

...computer SOP and compiling methods with various novel features. These features are 1) directly mapped and hardwired application program, 2) **extraction** of high degree of **parallelism** by exploiting operation and function level parallelism, 3) large **data structure** manipulation with functional memory, 5) token driven distributed small and high speed control circuit.

6/3,K/3 (Item 3 from file: 2)
DIALOG(R)File 2:INSPEC
(c) 2010 The IET. All rts. reserv.

04703253

Title: Construction of interfaces for the exchange of geographic data
Author(s): Pascoe, R.T. 1; Penny, J.P. 1
Affiliation(s):
1. Dept. of Comput. Sci., Canterbury Univ., Christchurch, New Zealand
Journal: International Journal of Geographical Information Systems, vol.4, no.2, pp.147-56

Country of Publication: UK
Publication Date: April-June 1990
ISSN: 0269-3798
ISSN Type: print
CODEN: IJGSE3
Language: English
Subfile(s): C (Computing & Control Engineering)
INSPEC Update Issue: 1990-019
Copyright: 1990, IEE

Abstract: Transferring data from one geographic information system to another commonly requires a sequence of interfaces, software packages that convert data from one format to another. Construction is described of...
...that uses a relational database management system and compiler-building tools that work from a machine-readable definition of a data format. Parallels are drawn with conversion and translation problems in other areas from which software tools might be obtained for automation of interface construction. Three interfacing strategies are...
...strategy of using a standard interchange format. The principal conclusion is that achieving a widely-accepted method of defining geographic data formats should be an important objective of efforts at standardization.

6/3,K/4 (Item 4 from file: 2)
DIALOG(R)File 2:INSPEC
(c) 2010 The IET. All rts. reserv.

04646804

Title: A methodology for implementing highly concurrent data structures
Author(s): Herlihy, M. 1
Affiliation(s):
1. Digital Equipment Corp., Cambridge Res. Center, MA, USA
Journal: SIGPLAN Notices, vol.25, no.3, pp.197-206
Country of Publication: USA
Publication Date: March 1990
Conference Title: Second ACM Sigplan Symposium on Principles and Practice of Parallel Programming
Conference Date: 14-16 March 1990
Conference Location: Seattle, WA, USA
Conference Sponsor: ACM
ISSN: 0362-1340
ISSN Type: print
CODEN: SINODQ

U.S. Copyright Clearance Center Code: 0 89791 350 7/90/0003/0197\$1.50
Language: English
Subfile(s): C (Computing & Control Engineering)
INSPEC Update Issue: 1990-013
Copyright: 1990, IEE

Abstract: A concurrent object is a **data structure** shared by concurrent processes. Conventional techniques for implementing concurrent objects typically rely on critical sections: ensuring that only one process at a time can operate on the object. Nevertheless, critical sections are poorly suited for asynchronous systems: if one process is halted or delayed in a critical section, other, non-faulty processes will be unable to progress. By contrast...

...free implementations of concurrent objects. The object's representation and operations are written as stylized sequential programs, with no explicit synchronization. Each sequential operation is automatically transformed into a non-blocking or wait-free operation using novel synchronization and memory management algorithms. These algorithms are presented for...

6/3,K/5 (Item 5 from file: 2)
DIALOG(R)File 2:INSPEC
(c) 2010 The IET. All rts. reserv.

03586826

Title: A three-dimensional computer for image and signal processing
Author(s): Little, M.J. 1; Nash, J.G. 1; Etchells, R.D. 1; Grinberg, J. 1; Nudd, G.R. 1

Affiliation(s):

1. Hughes Res. Labs., Malibu, CA, USA

Book Title: Proceedings of the IEEE 1985 Custom Integrated Circuits Conference (Cat. No. 85CH2157-6)

Inclusive Page Numbers: 119-23

Publisher: IEEE, New York, NY

Country of Publication: USA

Publication Date: 1985

Conference Title: IEEE 1985 Custom Integrated Circuits Conference

Conference Date: 20-23 May 1985

Conference Location: Portland, OR, USA

Conference Sponsor: IEEE

U.S. Copyright Clearance Center Code: CH2157-6/85/0000-0119\$01.00

Number of Pages: 527

Language: English

Subfile(s): B (Electrical & Electronic Engineering); C (Computing & Control Engineering)

INSPEC Update Issue: 1986-003

Copyright: 1986, IEE

Abstract: The authors are engaged in developing a computer that is optimized for use on two-dimensionally structured data. This machine is a cellular array computer with one processor for each pixel or matrix element of the input data. Cellular array computers have been built in the past...

...circuitry, one on top of another, to form a three-dimensionally integrated computer. Additional benefits, beyond high data throughput, are derived from these massively parallel communication channels. These additional benefits include modular construction, fault tolerance, compact size, low power consumption, and flexible architecture.

B. NPL Files, Full-text

Non-Patent Literature: Full Text

Dialog files: 9,13,15,16,20,75,148,160,275,610,613,621,624,634,636,647,674,810,813

File 9:Business & Industry(R) Jul/1994-2010/Oct 29
(c) 2010 Gale/Cengage

File 13:BAMP 2010/Oct 29
(c) 2010 Gale/Cengage

File 15:ABI/Inform(R) 1971-2010/Oct 30
(c) 2010 ProQuest Info&Learning

File 16:Gale Group PROMT(R) 1990-2010/Oct 28
(c) 2010 Gale/Cengage

File 20:Dialog Global Reporter 1997-2010/Nov 01
(c) 2010 Dialog

File 75:TGG Management Contents(R) 86-2010/Oct W4
(c) 2010 Gale/Cengage

File 148:Gale Group Trade & Industry DB 1976-2010/Oct 29
(c) 2010 Gale/Cengage

File 160:Gale Group PROMT(R) 1972-1989
(c) 1999 The Gale Group

File 275:Gale Group Computer DB(TM) 1983-2010/Sep 17
(c) 2010 Gale/Cengage

File 610:Business Wire 1999-2010/Nov 01
(c) 2010 Business Wire.

File 613:PR Newswire 1999-2010/Nov 01
(c) 2010 PR Newswire Association Inc

File 621:Gale Group New Prod.Annou.(R) 1985-2010/Sep 08
(c) 2010 Gale/Cengage

File 624:McGraw-Hill Publications 1985-2010/Nov 01

(c) 2010 McGraw-Hill Co. Inc
 File 634:San Jose Mercury Jun 1985-2010/Oct 31
 (c) 2010 San Jose Mercury News
 File 636:Gale Group Newsletter DB(TM) 1987-2010/Oct 27
 (c) 2010 Gale/Cengage
 File 647:UBM Computer Fulltext 1988-2010/Oct W4
 (c) 2010 UBM, LLC
 File 674:Computer News Fulltext 1989-2006/Sep W1
 (c) 2006 IDG Communications
 File 810:Business Wire 1986-1999/Feb 28
 (c) 1999 Business Wire
 File 813:PR Newswire 1987-1999/Apr 30
 (c) 1999 PR Newswire Association Inc

Set	Items	Description
S1	16011784	(CONVERT? OR CONVERSION? ? OR TRANSLAT? OR REFORMAT? OR RE-CONFIGUR? OR RESTRUCTUR? OR TRANSFORM? OR TRANSPOS? OR PARSE? ? OR PARSING OR EXTRACT? OR DERIV? OR FILTER?)
S2	152871	S1(5N)(STREAMING OR PARALLEL? OR CONTEMPORANE? OR SYNCHRON? OR (SAME OR ONE)() (TIME OR INSTANT OR MOMENT) OR IMMEDIAT? OR CONCURRENT? OR COINCIDENT? OR SIMULTANE?)
S3	5724	(DATA OR FILE)(2N)(FORMAT? ? OR FORMATT? OR STRUCTUR? OR M-ODEL? OR PARADIGM? OR STANDARD? OR CONFIGUR? OR PATTERN? ? OR ARRANG?)
S4	17336	(FIRST OR ONE OR PRIME OR PRIMARY OR SECOND? OR TWO OR DIF-FER? OR SEPARAT? OR DISTINCT? OR PLURAL? OR MULTIPLE? ? OR MU-LTI OR ANOTHER)(2N)(SYSTEM? ? OR PLATFORM? ? OR COMPUTER? ? OR MACHINE? ? OR EQUIPMENT OR CONTROLLER?)
S5	49	S2(S)S3(S)S4
S6	36	S5 NOT PY>2004
S7	26	RD (unique items)

7/3,K/1 (Item 1 from file: 13)
 DIALOG(R)File 13:BAMP
 (c) 2010 Gale/Cengage. All rts. reserv.

00702845 Supplier Number: 25737750 (USE FORMAT 7 OR 9 FOR FULLTEXT)
 E-Procurement Standards: The hunt for interoperability: Part 1 of 2 parts
 (Lack of standard e-procurement standards can raise costs and make companies depend on third-party service providers)
 Article Author(s): Porter, Anne Millen
 Purchasing, v 128, n 10, p S50-S65
 June 15, 2000
 DOCUMENT TYPE: Journal ISSN: 0033-4448 (United States)
 LANGUAGE: English RECORD TYPE: Fulltext
 WORD COUNT: 2962

(USE FORMAT 7 OR 9 FOR FULLTEXT)

TEXT:

...to move from one XML standard to another.

O'Connor elaborates: "I think we'll see an emergence of XML translators built into systems architectures paralleling the work to set data standards. Translators will allow for flexibility in having multiple standards, but this will add expense and will create opportunities for data...

...The question, he says, comes down to " 'How many hoops does data have to jump through to move from your system to mine?' Multiple XML standards requiring translators will simply represent another hoop." That said, O'Connor suggests that it's more realistic to...

7/3,K/2 (Item 1 from file: 15)
DIALOG(R)File 15:ABI/Inform(R)
(c) 2010 ProQuest Info&Learning. All rts. reserv.

02161127 73027407
Flipping for FlipFactory
Perey, Christine
Network World v18n20 PP: 49-50 May 14, 2001
ISSN: 0887-7661 JRNL CODE: NWW
WORD COUNT: 2099

...TEXT: audio media from one format - a high-quality, low-compressed or uncompressed original, (such as from a digital video editing system) - to another format suitable for IP-based streaming (or from one format to multiple data rates and formats). Industry-leading solutions for performing these conversions (most notably Media 100's Cleaner Pro and Avid's ePublisher) are designed...

...same person who edits, titles and "polishes" the content to meet corporate communications guidelines typically will be involved in the conversion to Web-ready/streaming format (also known as encoding).

Many media professionals spend hours bent over a keyboard and mouse in an editing bay...

7/3,K/3 (Item 2 from file: 15)
DIALOG(R)File 15:ABI/Inform(R)

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01013845 96-63238

Software helps span distributed management gulf

Larsen, Amy K

Data Communications v24n6 PP: 43-44 May 1995

ISSN: 0363-6399 JRNL CODE: DCM

ABSTRACT: Administering distributed networks is enough of a trick; add multiple management systems that do not talk to one another and net managers almost have to resort to sorcery to share configuration data across the enterprise. With EventIX Mapsync 2.0 from Bridgeway Corp., black magic becomes unnecessary. The software lets managers share topology and event information among several management systems and monitor separately administered domains from one map. The new version of Mapsync is a follow-up to the Mapsync 1.0 package introduced in fall 1994. It is a conversion application that synchronizes topology data for management platforms from the same vendor. It also configures information from different vendors' SNMP management platforms and...

7/3,K/4 (Item 3 from file: 15)

DIALOG(R)File 15:ABI/Inform(R)

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00842803 94-92195

Massively parallel methods for engineering and science problems

Camp, W J; Plimpton, S J; Hendrickson, B A; Leland, R W

Communications of the ACM v37n4 PP: 30-41 Apr 1994

ISSN: 0001-0782 JRNL CODE: ACM

WORD COUNT: 6742

...TEXT: executes on its local data. Processors cooperate to exchange data in the form of messages sent from one processor to another. Because MIMD computers store code on each processor, they are capable of running with no "front-end." The exchange of data messages in...

...not view the run-time generation of message passing (triggered by a remote memory access) as an efficient way to extract maximum parallelism from an application. While system support for these operations is provided on machines like the Cray T3D and KSR-1, the user can almost always get much better performance by structuring the data and operations in the code to better reflect the parallelism inherent in the problem being solved.

By contrast, explicit message...

7/3,K/5 (Item 1 from file: 16)
DIALOG(R)File 16:Gale Group PROMT(R)
(c) 2010 Gale/Cengage. All rts. reserv.

08634028 Supplier Number: 74573171 (USE FORMAT 7 FOR FULLTEXT)
Flipping for FlipFactory; Telestream software makes multimedia file
conversion a breeze.(Software Review)(Evaluation)
Perey, Network World Global Test Alliance Christine
Network World, p49
May 14, 2001
Language: English Record Type: Fulltext
Article Type: Evaluation
Document Type: Magazine/Journal; General Trade
Word Count: 1966

... same person who edits, titles and "polishes" the content to meet
corporate communications guidelines typically will be involved in the
conversion to Web-ready/**streaming** format (also known as
encoding).

Many media professionals spend hours bent over a keyboard and mouse
in an editing bay...

7/3,K/6 (Item 2 from file: 16)
DIALOG(R)File 16:Gale Group PROMT(R)
(c) 2010 Gale/Cengage. All rts. reserv.

07483482 Supplier Number: 62774412 (USE FORMAT 7 FOR FULLTEXT)
The hunt for interoperability.
Porter, Anne Millen
Purchasing, v128, n10, pS50
June 15, 2000
Language: English Record Type: Fulltext
Document Type: Magazine/Journal; Trade
Word Count: 6211

... The question, he says, comes down to " 'How many hoops does data
have to jump through to move from your **system** to mine?'
Multiple XML standards requiring translators will simply represent
another hoop." That said, O'Connor suggests that it's more realistic to...

7/3,K/7 (Item 3 from file: 16)
DIALOG(R)File 16:Gale Group PROMT(R)
(c) 2010 Gale/Cengage. All rts. reserv.

05920324 Supplier Number: 53153465 (USE FORMAT 7 FOR FULLTEXT)
Agile Software Receives Oracle CAI Certification for its Supply Chain
Collaboration Solution Integrated with Oracle Applications Release 11.
PR Newswire, p3928
Nov 2, 1998
Language: English Record Type: Fulltext
Document Type: Newswire; Trade
Word Count: 854

... database fields to the corresponding fields of the ERP system and
converts file formats to permit automatic synchronization between the
two systems.

"The standard industry approach is to perform custom integrations
with ERP systems, which increase the time and cost of the...

7/3,K/8 (Item 4 from file: 16)
DIALOG(R)File 16:Gale Group PROMT(R)
(c) 2010 Gale/Cengage. All rts. reserv.

05509922 Supplier Number: 48348626 (USE FORMAT 7 FOR FULLTEXT)
Agile Software, an Oracle Cooperative Applications Initiative(SM) (CAI)
Member, Receives Oracle(R) 10.7 Certification for Product Data Management
(PDM) Suite
PR Newswire, p0310LATU047
March 10, 1998
Language: English Record Type: Fulltext
Document Type: Newswire; Trade
Word Count: 1111

... database fields to the corresponding fields of the ERP system and
converts file formats to permit automatic synchronization between the
two systems.

"Our PDM competitors usually perform custom integrations with ERP
systems, which increase the time and cost of the implementation," says...

7/3,K/9 (Item 1 from file: 20)
DIALOG(R)File 20:Dialog Global Reporter
(c) 2010 Dialog. All rts. reserv.

32506426
Ascential Software Analyst Day - Part 1
FAIR DISCLOSURE WIRE
November 12, 2003
JOURNAL CODE: WFDW LANGUAGE: English RECORD TYPE: FULLTEXT

WORD COUNT: 4701

... output of my business intelligence reporting system. Of course, it also provides the ability to develop business rules about data transformation and data integration one time and reuse them everywhere and anywhere within the enterprise. This addresses one of the really big challenges with internally developed...the traditional competitors like Informatica, but their solutions are focused only on the transformation capability. There are vendors who offer data standardization and cleansing capabilities but don't offer profiling or don't offer transformation. So, our ability to bring this all...

7/3,K/10 (Item 2 from file: 20)
DIALOG(R)File 20:Dialog Global Reporter
(c) 2010 Dialog. All rts. reserv.

28245351
Seagate and Best Buy First to Offer Serial ATA Hard Drive in Retail
CANADA NEWSWIRE
March 24, 2003
JOURNAL CODE: WCNW LANGUAGE: English RECORD TYPE: FULLTEXT
WORD COUNT: 787

...ATA in all PCs. Unlike other Serial ATA hard drives in development, Seagate's native Serial ATA technology contains no parallel -to-serial translator bridges that may reduce performance. Seagate is the worldwide leader in the design, manufacturing and marketing of hard disc drives...

7/3,K/11 (Item 3 from file: 20)
DIALOG(R)File 20:Dialog Global Reporter
(c) 2010 Dialog. All rts. reserv.

01432964 (USE FORMAT 7 OR 9 FOR FULLTEXT)
Decision.ism Announces "On Oracle" Relationship
BUSINESS WIRE
April 21, 1998 9:47
JOURNAL CODE: WBWE LANGUAGE: English RECORD TYPE: FULLTEXT
WORD COUNT: 664

(USE FORMAT 7 OR 9 FOR FULLTEXT)

... called dimensions. Dimensions such as time, product, and geography categorize and summarize facts, such as unit sales. Accompanying the multidimensional data model are mathematical, financial and

statistical functions that analyze, forecast, model and allow "what-if" questions about the data. Oracle Express...

7/3,K/12 (Item 1 from file: 148)
DIALOG(R)File 148:Gale Group Trade & Industry DB
(c) 2010 Gale/Cengage. All rts. reserv.

14847297 SUPPLIER NUMBER: 89968336 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Tower of Babel -- CAD style. (Software Review).(differing formats in
CAD)(Brief Article)
British Plastics & Rubber, 25(1)
June-July, 2002
DOCUMENT TYPE: Brief Article ISSN: 0307-6164 LANGUAGE: English
RECORD TYPE: Fulltext
WORD COUNT: 1074 LINE COUNT: 00086

... credit or debit card online to Delcam's secure server. There is no waiting; the vouchers are generated automatically and immediately.

Whenever you try to ~~translate~~ a new file, a Wizard in PS-Exchange detects it and responds by connecting your browser to the Delcam site...

7/3,K/13 (Item 2 from file: 148)
DIALOG(R)File 148:Gale Group Trade & Industry DB
(c) 2010 Gale/Cengage. All rts. reserv.

08124425 SUPPLIER NUMBER: 17389671 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Plastics technology: manufacturing handbook & buyers' guide 1995/96.(Buyers Guide)
Plastics Technology, v41, n8, pCOV(941)
August, 1995
DOCUMENT TYPE: Buyers Guide ISSN: 0032-1257 LANGUAGE: English
RECORD TYPE: Fulltext
WORD COUNT: 174436 LINE COUNT: 15187

... 02 adds to the basic system five automatic downtime-reason machine inputs and batch-counter capability. MDT-11 adds numeric data display capability. MDT-20 adds 40-character alphanumeric display; keypad for operator data entry; 20 downtime and 20 reject reason...inputs. Can include automatic heater checking, process and deviation alarms, and ramp-and-soak programming. Digital I/Os control other machine functions.

BARBER-COLMAN CO. INDUSTRIAL INSTRUMENTS DIV.

MACO 4000 series injection molding control systems, MACO 5000 series extrusion control systems...

...may have its own 100-step profile. As many of the 100 steps as desired can be entered, allowing the **system** to interpolate the remaining points either in a flat linear or hyperbolic fashion. MACO 6500 16-slot control-card chassis is standard, and a **multiple**-chassis capability provides control for larger machines requiring additional temperature, I/O, timers, counters, parison control, linear-positioning control, and... EUROTHERM CONTROLS, INC.

EM-2 Extrusion Master complete extrusion control system is based on totally distributed architecture, ensuring minimum downtime. **System** integrates temperature control, melt-pressure control, melt-pump control, alarms, and interlocks into a single package, which also can supervise... including retrofits.

HES temperature controller utilizes a patented dual-thermocouple design to maintain precise temperature control in critical production processes. **System** uses **two** thermocouples in each barrel zone. With one thermocouple located in the heater/cooler and the other at the deepest point...

7/3,K/14 (Item 3 from file: 148)
DIALOG(R)File 148:Gale Group Trade & Industry DB
(c) 2010 Gale/Cengage. All rts. reserv.

06068103 SUPPLIER NUMBER: 12675177
Sync Research upgrades its SNA net access products; SNAC offering can now support more devices. (SNA Network Access Controllers now use Intel 80486 microprocessor)
Molloy, Maureen
Network World, v9, n35, p19(1)
August 31, 1992
ISSN: 0887-7661 LANGUAGE: ENGLISH RECORD TYPE: ABSTRACT

...ABSTRACT: and other devices for Token Ring local area networks (LANs), and the SNAC/Group Poll Concentrator, which concentrates traffic from **multiple cluster controllers** onto a single SDLC multipoint or point-to-point trunk.

7/3,K/15 (Item 4 from file: 148)
DIALOG(R)File 148:Gale Group Trade & Industry DB
(c) 2010 Gale/Cengage. All rts. reserv.

04160096 SUPPLIER NUMBER: 08270553 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Developing the process plan. (Computer-Assisted Manufacturing Planning and Control for Job Shops, part 3)
Diesslin, Rich; O'Connor, Fran
Modern Machine Shop, v62, n7, p98(9)

Dec, 1989
ISSN: 0026-8003 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT
WORD COUNT: 2328 LINE COUNT: 00186

... using the new system immediately. Although this immediate conversion put pressure on users, they had the advantage of moving from one automated system to another. This prior experience eased the transition.

Meyer Tool developed its own prototype Process Planning module, designed to introduce gradually the...

7/3,K/16 (Item 5 from file: 148)
DIALOG(R)File 148:Gale Group Trade & Industry DB
(c) 2010 Gale/Cengage. All rts. reserv.

03892150 SUPPLIER NUMBER: 07259552 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Minigrams.
Computergram International, n1176, CGI05150021
May 15, 1989
ISSN: 0268-716X LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT
WORD COUNT: 2250 LINE COUNT: 00179

... VAX host - a single drive can be used but can't be accessed by both operating systems hosts at the same time - the interface must be physically reconfigured; the magneto-optical Inspire drive is \$7,500 for MS-DOS machines, \$8,900 for VAX Unibus, or \$8,500...

7/3,K/17 (Item 1 from file: 160)
DIALOG(R)File 160:Gale Group PROMT(R)
(c) 1999 The Gale Group. All rts. reserv.

01027474
BEST BITS: Translating data.
IEEE Spectrum May, 1984 p. 22

... Massachusetts) developed a computerized office equipment interface device that uses a Z80 8-bit microprocessor and 2 serial and 2 parallel data ports to convert protocols and data formats of one brand of computer into those of another. Able to accept information from equipment made by about 30 different vendors, the system first loads data from a disc or from another computer's output port into a memory buffer, where translation into 'A-code' takes place. From there, the data are converted into a target format and are stored on disc or transmitted to another computer. Applications include transferring information between

otherwise incompatible discs, loading text from one or more word processors into a typesetting machine...

7/3,K/18 (Item 1 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
(c) 2010 Gale/Cengage. All rts. reserv.

01950643 SUPPLIER NUMBER: 18243142 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Axon FlexiProbe 6000. (Axial Systems' Axon FlexiProbe 6000 multiport RMON probe)(Product Announcement)(Brief Article)
PC User, n278, p22(1)
March 6, 1996
DOCUMENT TYPE: Product Announcement Brief Article ISSN: 0263-5720
LANGUAGE: English RECORD TYPE: Fulltext
WORD COUNT: 258 LINE COUNT: 00023

... The i960 RISC processor-based system is designed for high performance, matched by 32Mb of RAM.

The FlexiProbe can be **configured** to perform **data** capture with **filtering**, protocol analysis and statistics reporting **simultaneously** for a number of **different** management **systems**.

It uses Dynamically Loadable Modules (DLMs) so that additional features such as traffic generation can be downloaded at any time...

7/3,K/19 (Item 2 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
(c) 2010 Gale/Cengage. All rts. reserv.

01679611 SUPPLIER NUMBER: 15309196 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Massively parallel methods for engineering and science problems. (High Performance Computing)
Camp, W.J.; Plimpton, S.J.; Hendrickson, B.A.; Leland, R.W.
Communications of the ACM, v37, n4, p31(11)
April, 1994
ISSN: 0001-0782 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT; ABSTRACT
WORD COUNT: 7014 LINE COUNT: 00582

... executes on its local data. Processors cooperate to exchange data in the form of messages sent from one processor to **another**. Because MIMD computers store on each processor, they are ...not view the run-time generation of message passing (triggered by a remote memory access) as an efficient way to **extract** maximum **parallel** from an application. While system support for these operations is provided on machine like the Cray T3D and KSR-1, the user can almost always get much

better performance by structuring the data and operations in the code to better reflect the parallelism inherent in the problem being solved.

By contrast, explicit message...

7/3,K/20 (Item 3 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
(c) 2010 Gale/Cengage. All rts. reserv.

01439758 SUPPLIER NUMBER: 10957512 (USE FORMAT 7 OR 9 FOR FULL TEXT)
MiniFinders. (buyer's guide to Apple Macintosh hardware and software)
(buyers guide)
MacUser, v7, n8, p135(49)
August, 1991
DOCUMENT TYPE: buyers guide ISSN: 0884-0997 LANGUAGE: ENGLISH
RECORD TYPE: FULLTEXT
WORD COUNT: 80288 LINE COUNT: 06476

... server for networked Macs, PacerShare is the fastest VAX server we tested. Extra functionality comes at a high price. Maximum simultaneous users: 500. Supports Ethernet but not LocalTalk. Requires VAX. 10 users, \$2,100; 20 users, \$3,750. Pacer Software, 7911... Manager sold by La Cie) lets you connect a phone, a fax, and a modem to the same phone line. Two- and three-device versions are available. It's intelligent enough to route incoming calls correctly. Outside callers can be given...Dimension database application. Although 4D Calc can't replace a full-featured spreadsheet program, its "hot" links to 4D databases immediately update shared information. The ability to exchange data as SYLK files between Excel and 4D is adequate, but formulas may...89)

File Force 4

File Force is a relational-database manager. Script editor provides a graphic way to view database file structures, establish file relations, and access layouts. Excellent documentation. Version 1.0.1 shipping. \$395. ACIUS, 10351 Bubb Rd., Cupertino, CA 95014. (408...

...Nov '89) * '87 Eddy

DESKTOP
PUBLISHING
Cheshire 4

Cheshire is an INIT that lets you copy and paste tab-delimited data into simple charts, which can be placed in a document. Many chart types are possible, typeface styles can be varied...including constant access to hundreds of fonts, DAs, Fkeys, and sounds. A world-class utility. Version 1.53 shipping. Requires System 6.0.3 or later. \$89.95. ALSoft, Inc., P.O. Box 926, Spring, TX77383. (713) 353-4090. (Mar '89...

7/3,K/21 (Item 4 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
(c) 2010 Gale/Cengage. All rts. reserv.

01389501 SUPPLIER NUMBER: 10376529 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Using ARTIC in PC-based distributed control systems. (80186-based IBM
RealTime Interface Coprocessor card software support and services)
Wilson, Fred
I&CS (Instrumentation & Control Systems), v63, n10, p47(3)
Oct, 1990
ISSN: 0746-2395 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT; ABSTRACT
WORD COUNT: 1808 LINE COUNT: 00157

...ABSTRACT: By using the 80186 microprocessor, ARTIC is programmable for on-board functions such as I/O data preparation, address searches, pattern matches on data, and protocol conversion. ARTIC enables one computer to simultaneously communicate with several plant-flow controllers, and as a result has become popular with data acquisition, monitoring, and supervisory control...

7/3,K/22 (Item 5 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
(c) 2010 Gale/Cengage. All rts. reserv.

01174422 SUPPLIER NUMBER: 04240833
Micro to mainframe: making the right connection.
Derfler, Frank J.Jr.
PC Week, v5, n9, p116(6)
May 13, 1986
ISSN: 0740-1604 LANGUAGE: ENGLISH RECORD TYPE: ABSTRACT

...ABSTRACT: before loading it back to the host to be recompiled and run; and data exchange. The problems in connecting the two different computer systems include the data arrangement on the communications line and the data alphabet used by the host, linking synchronous (host) and asynchronous transmissions, and handling simultaneous translations and conversions. Potential solutions to these problems and the outlook for micro-to-mainframe links are discussed.

7/3,K/23 (Item 1 from file: 613)
DIALOG(R)File 613:PR Newswire
(c) 2010 PR Newswire Association Inc. All rts. reserv.

01067596 20031110SFM005 (USE FORMAT 7 FOR FULLTEXT)
Verity Announces Enhanced Version of KeyView Software
PR Newswire
Monday, November 10, 2003 07:00 EST
JOURNAL CODE: PR LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT
DOCUMENT TYPE: NEWSWIRE
WORD COUNT: 1,187

...WordPerfect, without having the
WordPerfect(R) Office application in her system.

Verity KeyView Filter SDK -- Extract Content and Metadata on
**Multiple
Platforms**

The Verity KeyView Filter SDK lets applications filter and extract text
from multiple formats on a wide variety of platforms...

...filter
without relying on filename extensions. As a result, applications can
interact
with information from different sources and in various **formats**. For
proprietary **file formats** not directly supported, the KeyView
Filter SDK also
provides customers the flexibility to quickly build custom filters to meet
their...

...SDK's threadsafe process design
eliminates the need to queue multiple documents for sequential filtering,
allowing multiple documents to be **filtered simultaneously**.

Verity Partner Program -- Maximize the Benefit of the Partnership
Verity KeyView OEM customers can take advantage of the Verity Partner
...

7/3,K/24 (Item 2 from file: 613)
DIALOG(R)File 613:PR Newswire
(c) 2010 PR Newswire Association Inc. All rts. reserv.

01000579 20030624LATU029 (USE FORMAT 7 FOR FULLTEXT)
CSG Systems Establishes Outsourced Billing
PR Newswire
Tuesday, June 24, 2003 08:33 EDT
JOURNAL CODE: PR LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT
DOCUMENT TYPE: NEWSWIRE
WORD COUNT: 783

TEXT:

...operations, capture new revenues faster and increase customer satisfaction.

CSG also will offer CSG Data Mediation in this service bureau model. CSG Data Mediation is the essential layer between a service provider's network and downstream applications that allows providers to capture the...

...effective platform that enables us to create a world class customer experience today and in the future."

Work will begin immediately to convert FairPoint's subscribers from its multiple billing systems onto the CSG ICMS platform and is expected to be completed in 2004. plaNet Consulting, a division of CSG that...

7/3,K/25 (Item 1 from file: 636)
DIALOG(R)File 636:Gale Group Newsletter DB(TM)
(c) 2010 Gale/Cengage. All rts. reserv.

01105348 Supplier Number: 40791916 (USE FORMAT 7 FOR FULLTEXT)
ALPHATRONIX HAS BYPASS
Computergram International, n1176, pN/A
May 15, 1989
Language: English Record Type: Fulltext
Document Type: Newswire; Trade
Word Count: 228

(USE FORMAT 7 FOR FULLTEXT)

ABSTRACT:

TEXT:

...DEC VAXes to share data via its Inspire erasable optical storage system: the utility gets around the problem of differing file formats on the two machine types by enabling data stored on a VAX under VMS operating to be written to the optical disk in an MS- DOS file format, and the company touts the combination as a fast, simple, and relatively low-cost alternative to network gateways, modems or...

...VAX host - a single drive can be used but can't be accessed by both operating systems hosts at the same time - the interface must be physically reconfigured; the magneto-optical Inspire drive is \$7,500 for MS-DOS machines, \$8,900 for VAX Unibus, or \$8,500...

7/3,K/26 (Item 1 from file: 674)
DIALOG(R)File 674:Computer News Fulltext
(c) 2006 IDG Communications. All rts. reserv.

049382

Make your move

Moving to Client/Server Messaging

As key products trickle out, it's time to assess whether to take the leap to client/server E-mail.

Byline: Elisabeth Horwitt

Journal: Network World Page Number: 44

Publication Date: January 22, 1996

Word Count: 2393 Line Count: 227

Text:

... storage engine. Clients must log on to the local post office, then download all waiting mail as a file. A **separate** DOS machine typically acts as the message transfer agent (MTA). It checks a post office's outgoing mailbox and routes nonlocal messages to other post offices, often across several intermediate MTAs. Corporations often need to install separate gateways for handling **translation** and directory **synchronization** across multivendor messaging systems. True client/server messaging platforms put far more intelligence on the back end. Typically, the jobs... says performance on DOS-based E-mail systems deteriorates rapidly. Faster throughput. Several factors make message delivery faster on client/server **systems**. **First**, since client/server systems support more users per server, a larger percentage of messages are local. Furthermore, nonlocal messages typically get sent right away to the correct server. In a **file-based configuration**, nonlocal messages typically wait in the local post office queue until the local MTA gets around to polling - usually five... is almost double that of OpenMail in a 20,000-user enterprise. The area that accounts for most of that **difference** is **system** integrity: ensuring that nodes are up and messages go through reliably. However, ``even in large companies, there is some argument...

V. Additional Resources Searched

No results were found in the Internet & Personal Computing Abstracts through EBSCO.
No results were found in the Financial Times through Proquest.